

# Compal Confidential

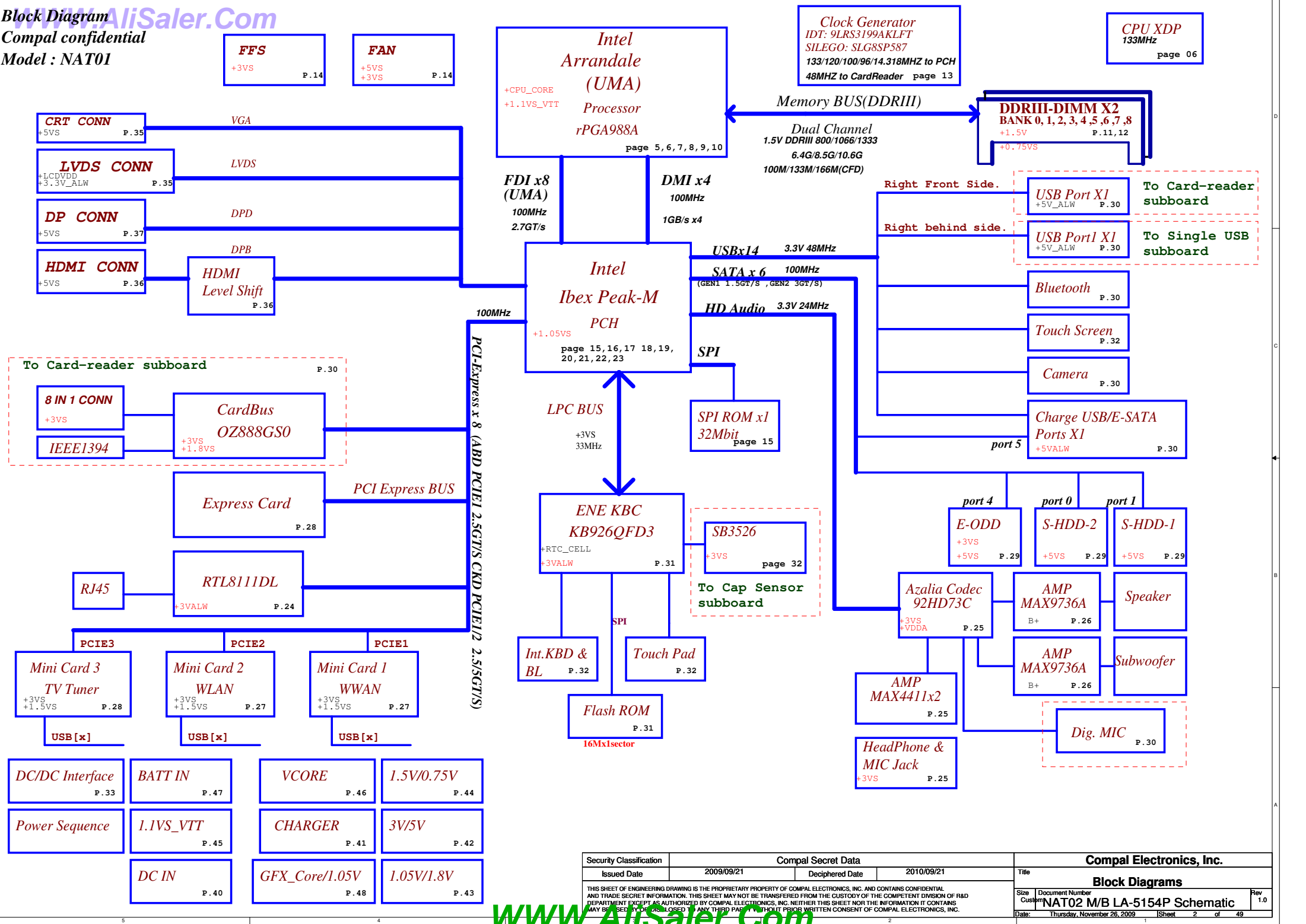
## NAT02 M/B Schematics Document

### Intel Arrandale Processor with DDRIII + Ibex Peak-M

2009-11-26

REV : 1 . 0

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2009/09/21	Deciphered Date	2010/09/21	Title		
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**Note : ON\* means that this power plane is ON only with AC power available, otherwise it is OFF.**

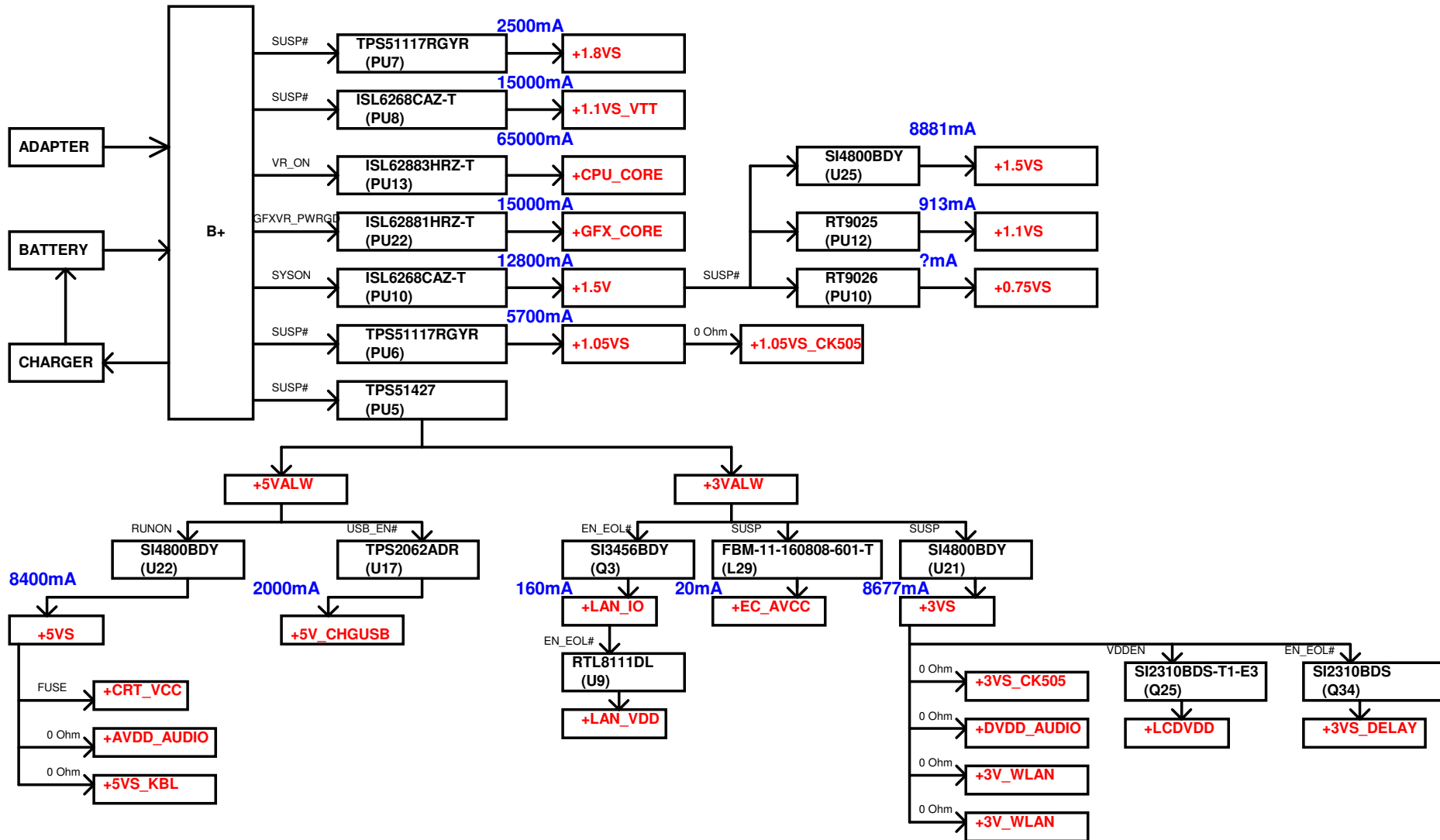
Device	IDSEL#	REQ#/GNT#	Interrupts
--------	--------	-----------	------------

Device	Address
Clock Generator (9LRS3191AKLFT, SLG8SP585)	1101 0010b
DDR DIMM0	1001 000Xb
DDR DIMM1	1001 010Xb
Free Fall Sensor	
CPU XDP	
PCH XDP	remove
XDCP_ISL90727	0101 110Xb
XDCP_ISL90728	0111 110Xb

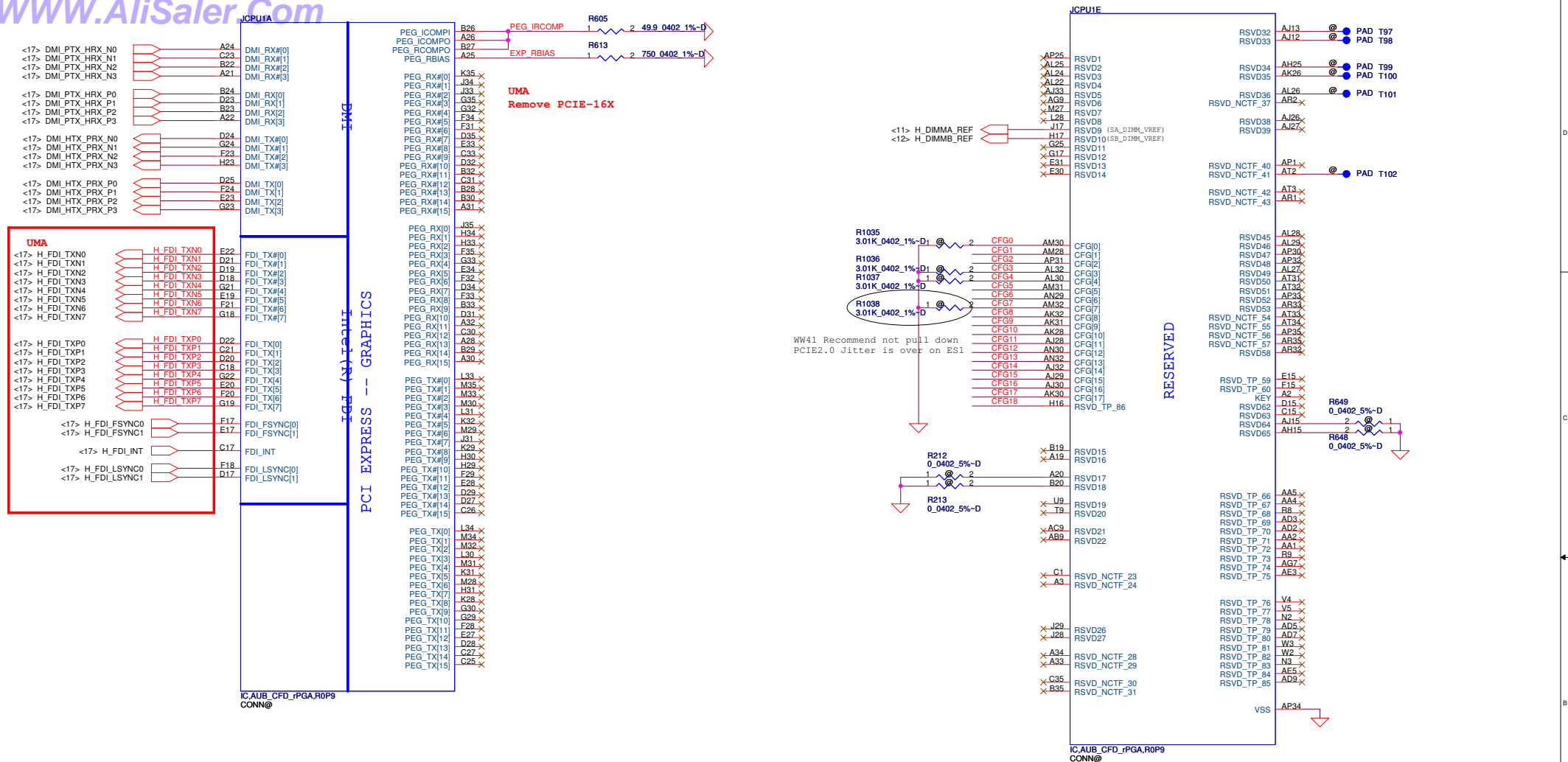
	Vcc	3.3V +/- 5%			
	Ra	100K +/- 1%			
Board ID	VGA	Rb	V <sub>AD_BID</sub> min	V <sub>AD_BID</sub> typ	V <sub>AD_BID</sub> max
X00	M96	0	0 V	0 V	0.100 V
X01	M96	8.2K +/- 1%	0.216 V	0.250 V	0.289 V
X02	M96	18K +/- 1%	0.436 V	0.503 V	0.538 V
MP	M96	33K +/- 1%	0.712 V	0.819 V	0.875 V
X00	Madison	56K +/- 1%	1.036 V	1.185 V	1.264 V
X01	Madison	100K +/- 1%	1.453 V	1.650 V	1.759 V
X02	Madison	200K +/- 1%	1.935 V	2.200 V	2.341 V
MP	Madison	NC	2.500 V	3.300 V	3.300 V

[illegible]

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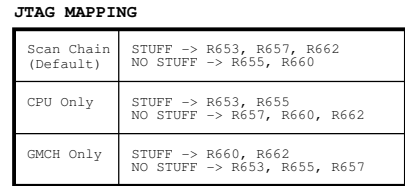


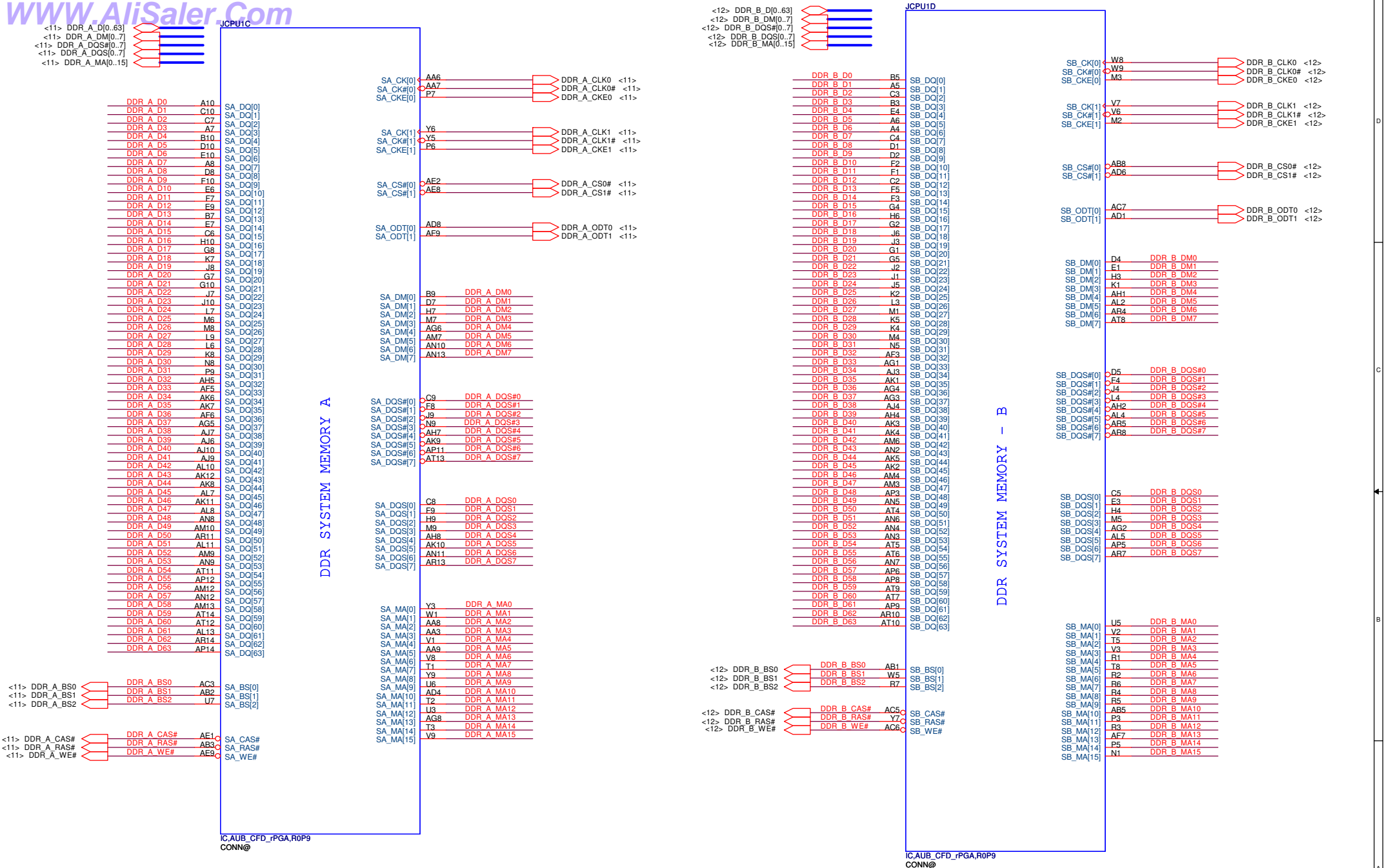
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								Power Rail			
Customer				Document Number				Rev			
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<b>CFG0 - PCI-Express Configuration Select</b>  *1:Single PEG 0:Bifurcation enabled	<b>CFG4 - Display Port Presence</b>  *1:Disabled; No Physical Display Port attached to Embedded Display Port 0:Enabled; An external Display Port device is connected to the Embedded Display Port
<b>CFG3 - PCI-Express Static Lane Reversal</b>  *1 :Normal Operation 0 :Lane Numbers Reversed 15 -> 0, 14 -> 1, ...	*:Default

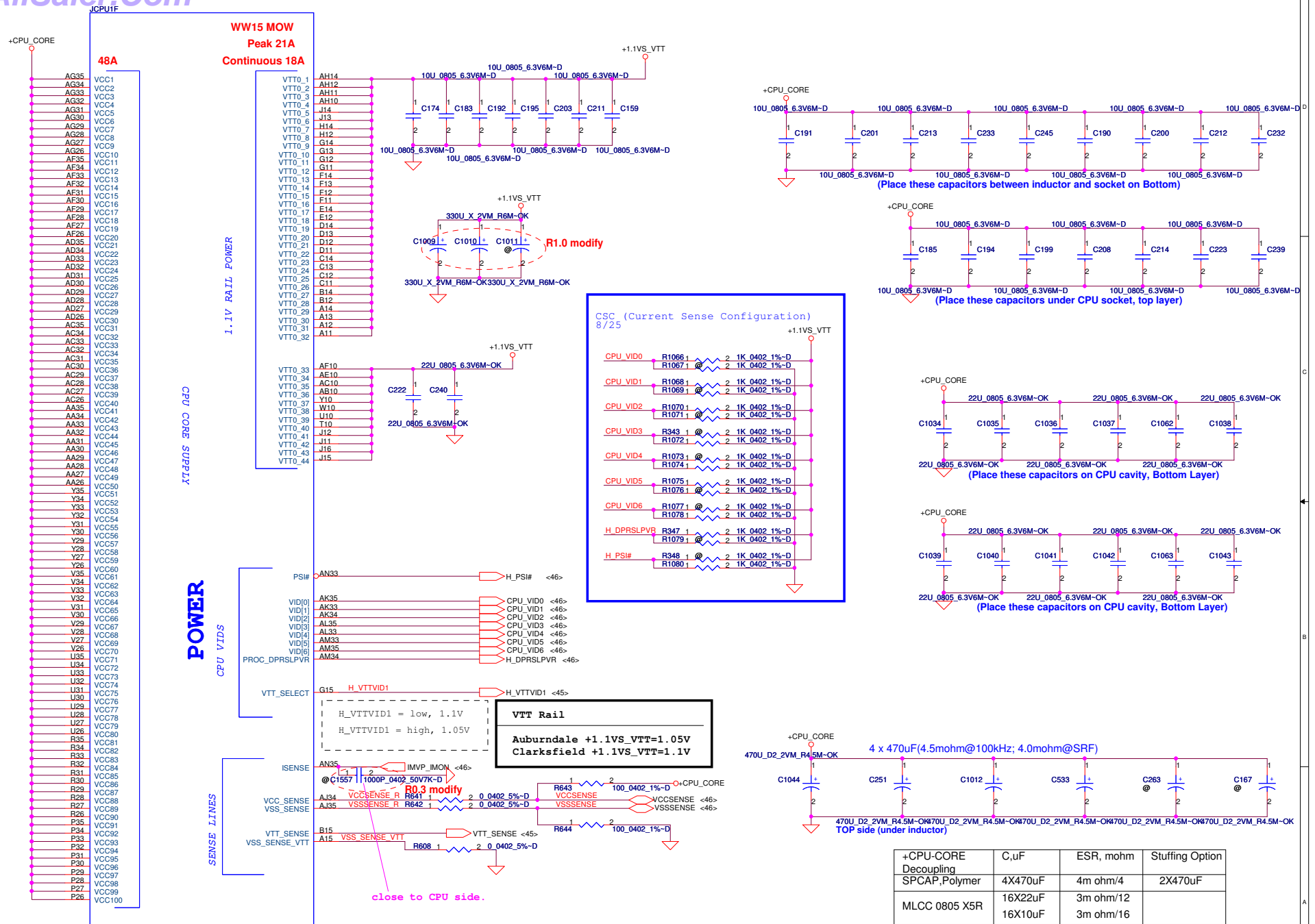
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/09/21	Deciphered Date	2010/09/21	Title	PROCESSOR (1/6) DMI,FDI,PEG
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				Customer	
				NAT02 M/B LA-5154P Schematic	
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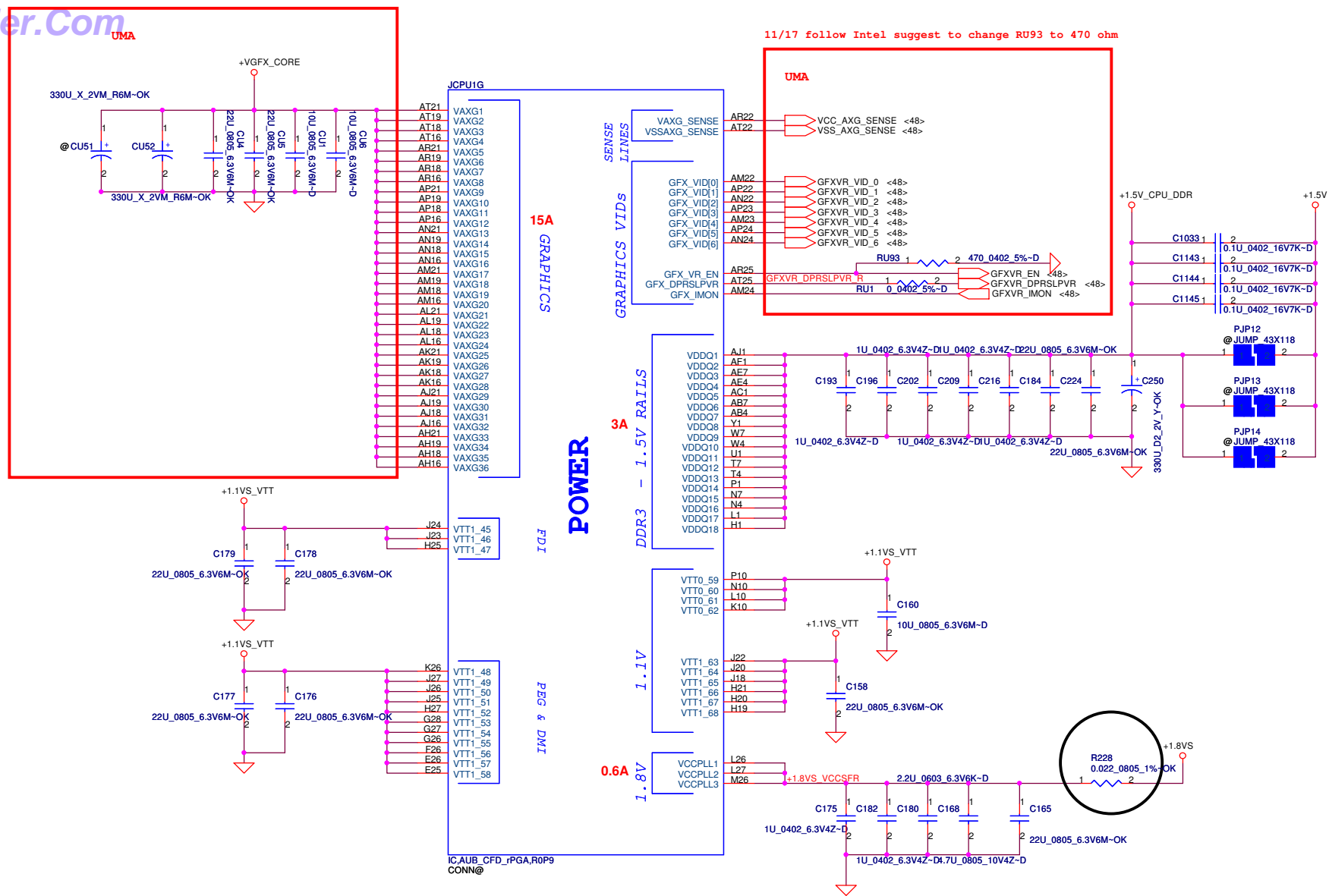


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								PROCESSOR (3/6) DDRIII			
Size		Document Number						Rev			
B		NAT02 M/B LA-5154P Schematic						1.0			
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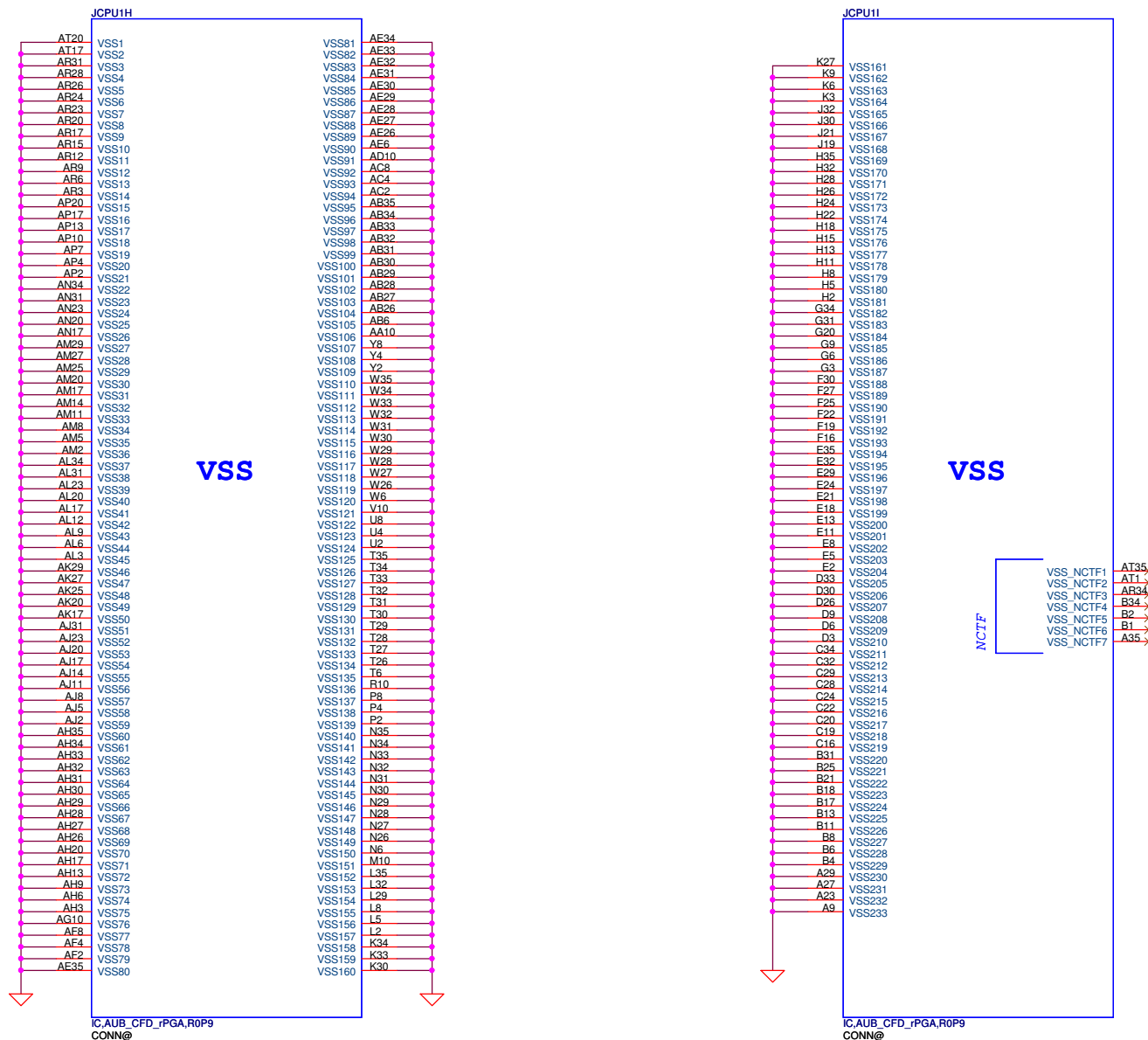








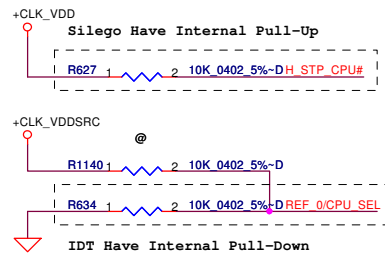
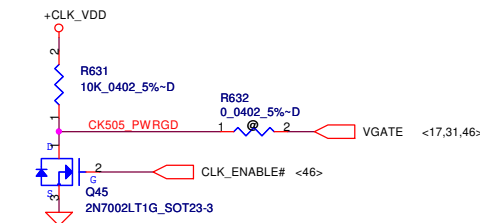
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Size		Document Number		Rev	
Customer		NAT02 M/B LA-5154P Schematic		1.0	
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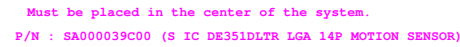
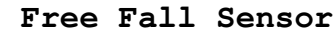
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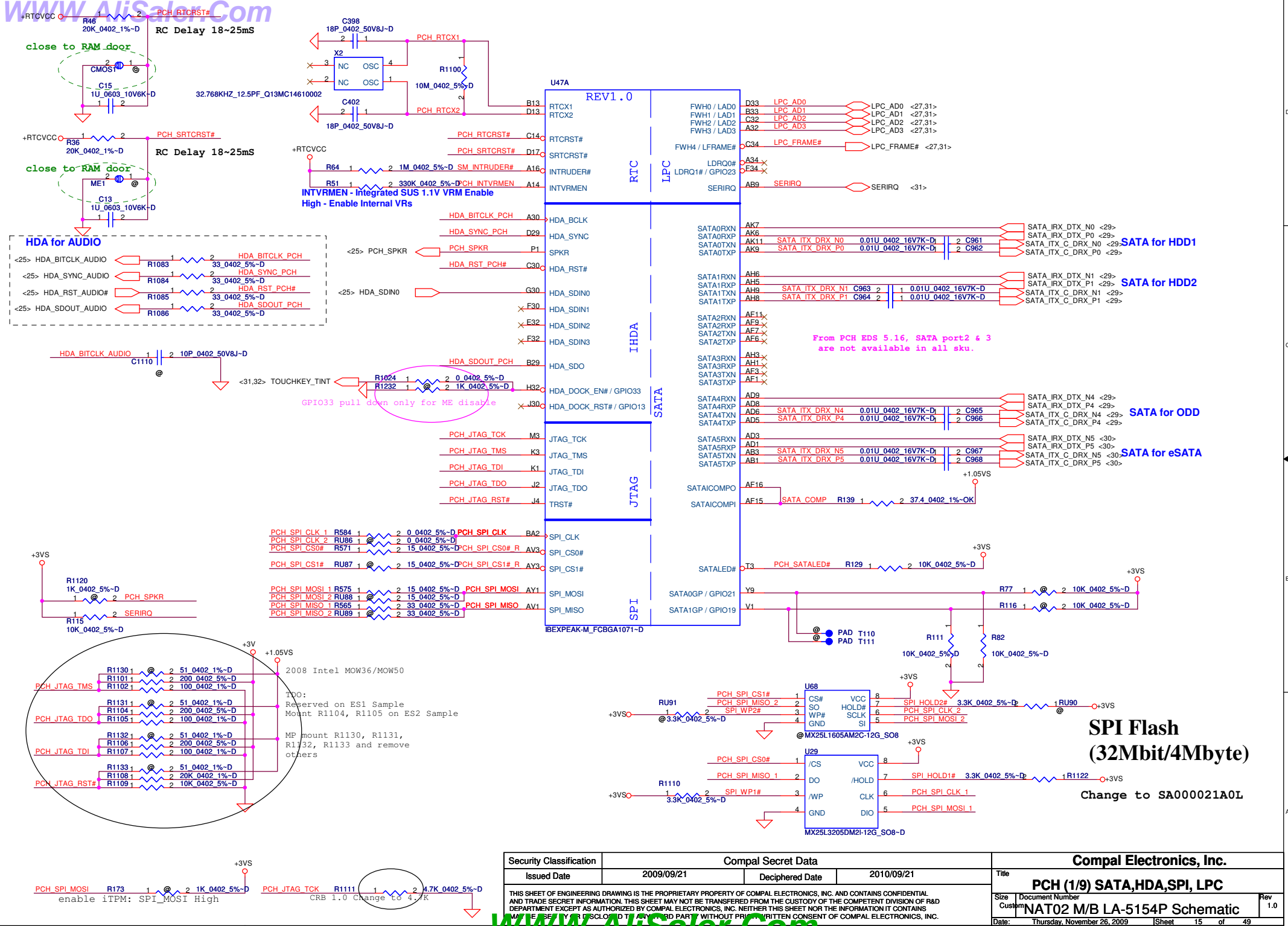




PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz

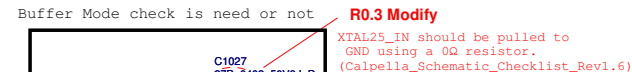
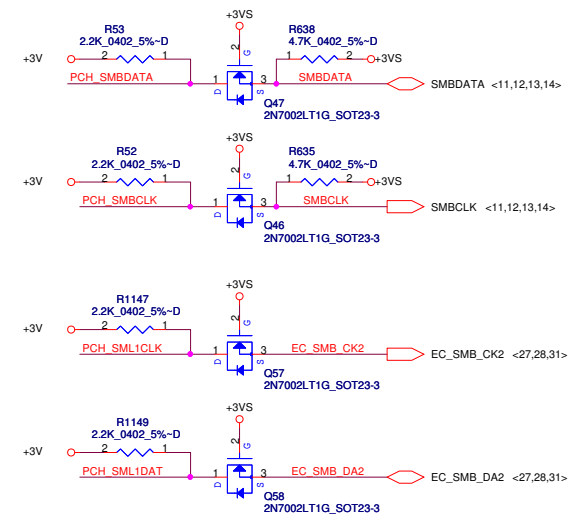
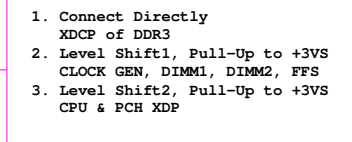


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				FAN & Screw Hole			
				Size	Document Number	Rev	
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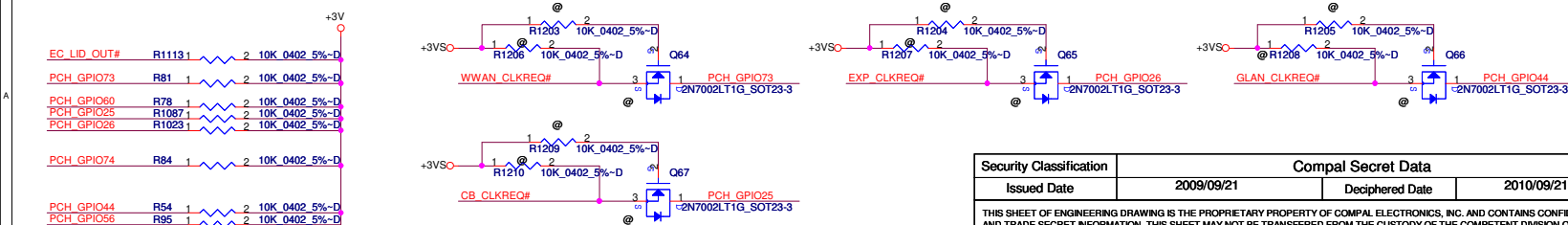


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								<b>PCH (1/9) SATA,HDA,SPI, LPC</b>			
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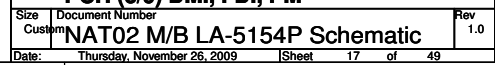


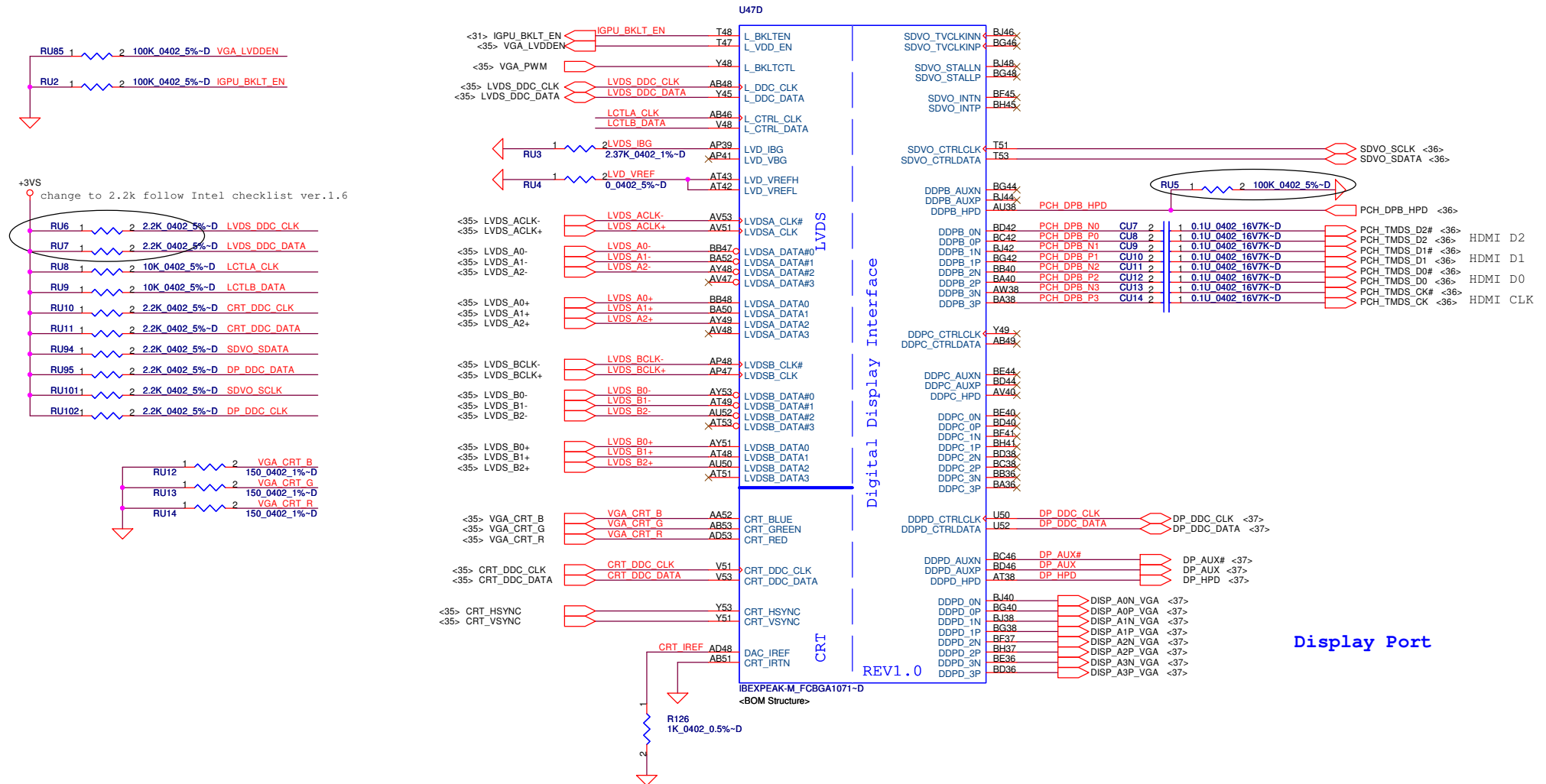


Note: ADD 25MHz  
crystal for Display Clock Integration



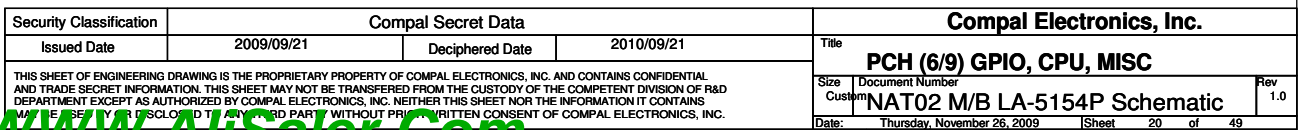
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Issued Date	2009/09/21	Deciphered Date	2010/09/21	Title	PCH (2/9) PCIE, SMBUS, CLK	
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				Date	Thursday, November 26, 2009	15:00

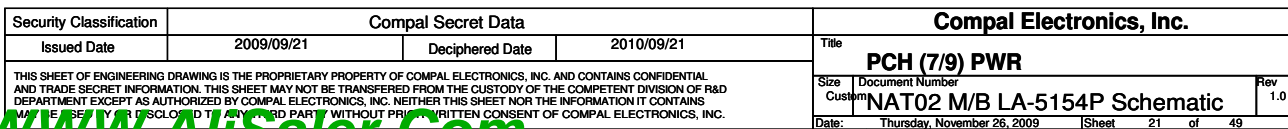




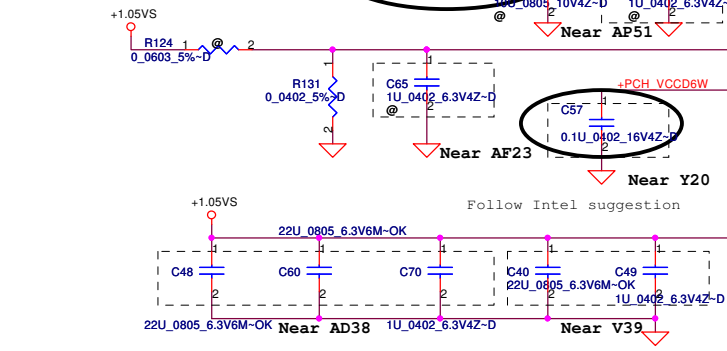
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Issued Date	2009/09/21	Deciphered Date	2010/09/21	Title	PCH (4/9) LVDS, CRT, DPI
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				Rev	1.0



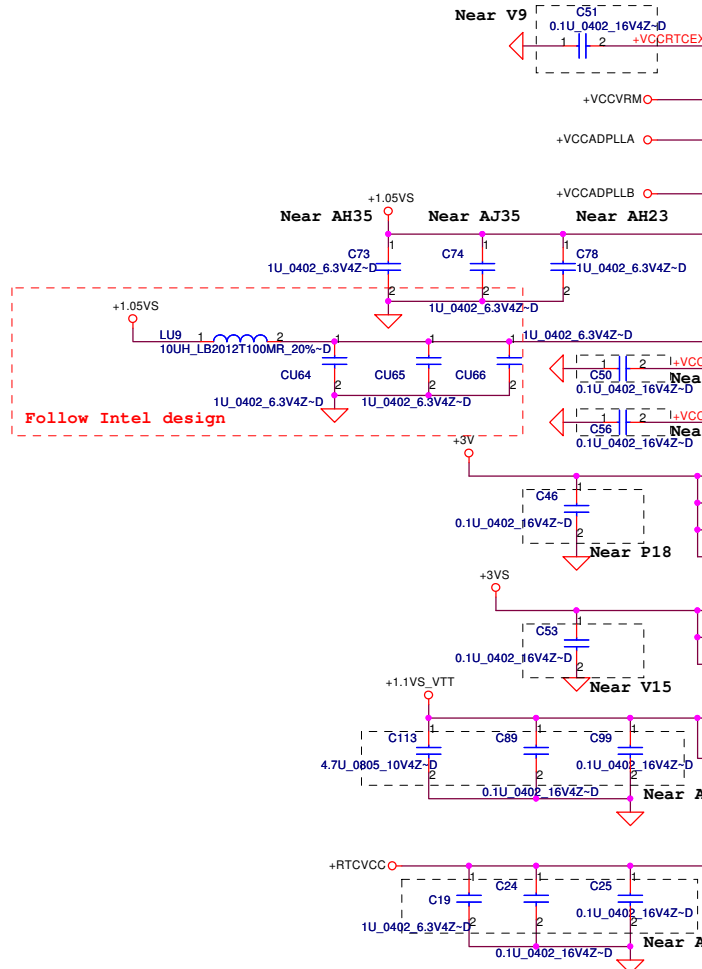








All Ix86 Peak-M Power rails with netnames +1.1VS and +1.1V rails are actually +1.05VS and +1.05V rails



# POWER

REV1.0

## USB

## Clock and Miscellaneous

## PCI/GPIO/LPC

## SATA

## PCI/GPIO/LPC

## CPU

## RTC

## HDA

## IBEXPEAK-M\_FCBGA1071-D

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## RTC

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AY7	VSS[159]	VSS[259]
B11	VSS[160]	VSS[260]
B15	VSS[161]	VSS[261]
B19	VSS[162]	VSS[262]
B23	VSS[163]	VSS[263]
B31	VSS[164]	VSS[264]
B35	VSS[165]	VSS[265]
B39	VSS[166]	VSS[266]
B43	VSS[167]	VSS[267]
B47	VSS[168]	VSS[268]
B7	VSS[169]	VSS[269]
BG12	VSS[170]	VSS[270]
BB12	VSS[171]	VSS[271]
BB16	VSS[172]	VSS[272]
BB20	VSS[173]	VSS[273]
BB24	VSS[174]	VSS[274]
BB30	VSS[175]	VSS[275]
BB34	VSS[176]	VSS[276]
BB38	VSS[177]	VSS[277]
BB42	VSS[178]	VSS[278]
BB49	VSS[179]	VSS[279]
BB5	VSS[180]	VSS[280]
BC10	VSS[181]	VSS[281]
BC14	VSS[182]	VSS[282]
BC18	VSS[183]	VSS[283]
BC2	VSS[184]	VSS[284]
BC22	VSS[185]	VSS[285]
BC32	VSS[186]	VSS[286]
BC36	VSS[187]	VSS[287]
BC40	VSS[188]	VSS[288]
BC44	VSS[189]	VSS[289]
BC52	VSS[190]	VSS[290]
BD49	VSS[191]	VSS[291]
BD48	VSS[192]	VSS[292]
BD49	VSS[193]	VSS[293]
BD5	VSS[194]	VSS[294]
BE12	VSS[195]	VSS[295]
BE16	VSS[196]	VSS[296]
BE20	VSS[197]	VSS[297]
BE24	VSS[198]	VSS[298]
BE30	VSS[199]	VSS[299]
BE34	VSS[200]	VSS[300]
BE38	VSS[201]	VSS[301]
BE42	VSS[202]	VSS[302]
BE46	VSS[203]	VSS[303]
BE48	VSS[204]	VSS[304]
BE50	VSS[205]	VSS[305]
BE6	VSS[206]	VSS[306]
BE8	VSS[207]	VSS[307]
BF3	VSS[208]	VSS[308]
BF49	VSS[209]	VSS[309]
BF51	VSS[210]	VSS[310]
BG18	VSS[211]	VSS[311]
BG24	VSS[212]	VSS[312]
BG4	VSS[213]	VSS[313]
BG50	VSS[214]	VSS[314]
BH11	VSS[215]	VSS[315]
BH15	VSS[216]	VSS[316]
BH19	VSS[217]	VSS[317]
BH23	VSS[218]	VSS[318]
BH31	VSS[219]	VSS[319]
BH35	VSS[220]	VSS[320]
BH39	VSS[221]	VSS[321]
BH43	VSS[222]	VSS[322]
BH47	VSS[223]	VSS[323]
BH7	VSS[224]	VSS[324]
C12	VSS[225]	VSS[325]
C50	VSS[226]	VSS[326]
D51	VSS[227]	VSS[327]
E12	VSS[228]	VSS[328]
E16	VSS[229]	VSS[329]
E20	VSS[230]	VSS[330]
E24	VSS[231]	VSS[331]
E30	VSS[232]	VSS[332]
E34	VSS[233]	VSS[333]
E38	VSS[234]	VSS[334]
E42	VSS[235]	VSS[335]
E46	VSS[236]	VSS[336]
E48	VSS[237]	VSS[337]
E6	VSS[238]	VSS[338]
F49	VSS[239]	VSS[339]
F5	VSS[240]	VSS[340]
G10	VSS[241]	VSS[341]
G14	VSS[242]	VSS[342]
G18	VSS[243]	VSS[343]
G2	VSS[244]	VSS[344]
G22	VSS[245]	VSS[345]
G32	VSS[246]	VSS[346]
G36	VSS[247]	VSS[347]
G40	VSS[248]	VSS[348]
G44	VSS[249]	VSS[349]
G52	VSS[250]	VSS[350]
AF39	VSS[251]	VSS[351]
H16	VSS[252]	VSS[352]
H20	VSS[253]	VSS[353]
H30	VSS[254]	VSS[354]
H34	VSS[255]	VSS[355]
H38	VSS[256]	VSS[356]
H42	VSS[257]	VSS[357]
H49	VSS[258]	VSS[358]

REV1.0

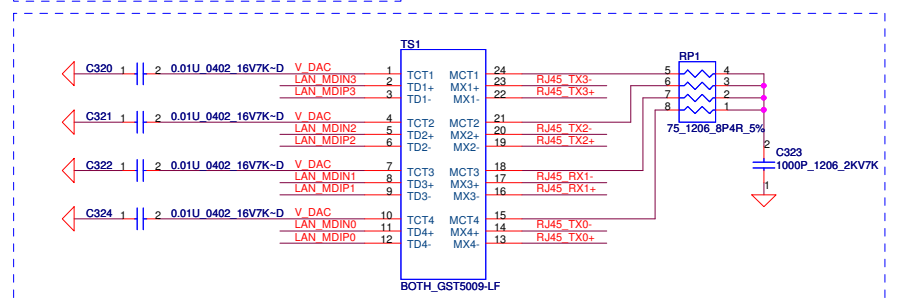
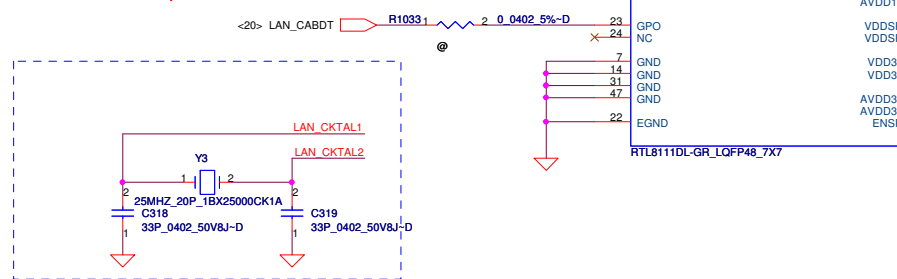
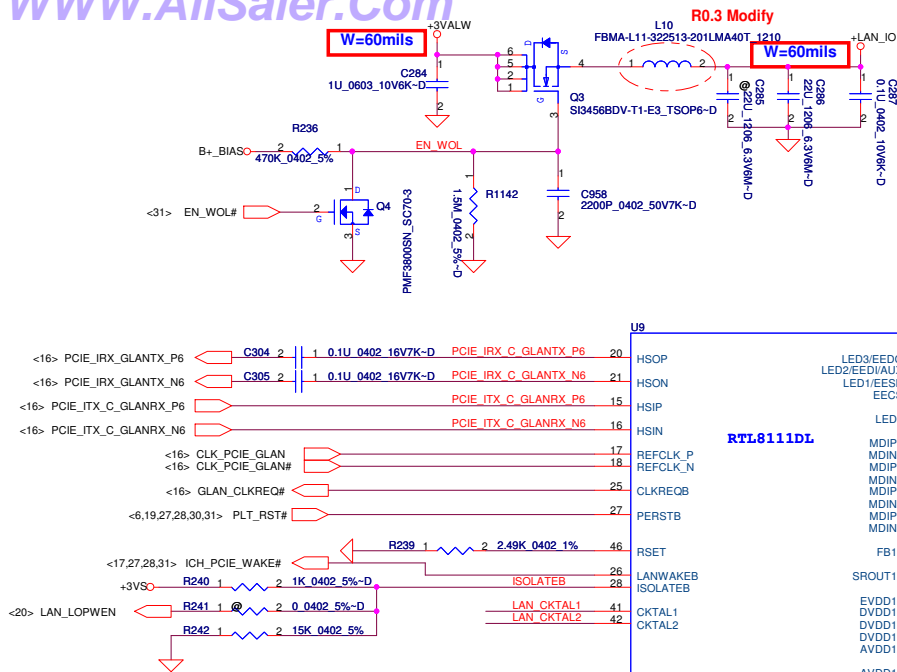
IBEXPEAK-M\_FCBGA1071-D

AB16	VSS[0]	VSS[80]	AK30
AA19	VSS[1]	VSS[81]	AK31
AA20	VSS[2]	VSS[82]	AK32
AA22	VSS[3]	VSS[83]	AK34
AM19	VSS[4]	VSS[84]	AK35
AA24	VSS[5]	VSS[85]	AK38
AA26	VSS[6]	VSS[86]	AK43
AA28	VSS[7]	VSS[87]	AK46
AA30	VSS[8]	VSS[88]	AK49
AA31	VSS[9]	VSS[89]	AK5
AA32	VSS[10]	VSS[90]	AK8
AB11	VSS[11]	VSS[91]	AL2
AB15	VSS[12]	VSS[92]	AL52
AB23	VSS[13]	VSS[93]	AM11
AB30	VSS[14]	VSS[94]	BB44
AB31	VSS[15]	VSS[95]	AD24
AB32	VSS[16]	VSS[96]	AM20
AB38	VSS[17]	VSS[97]	AM22
AB43	VSS[18]	VSS[98]	AM24
AB47	VSS[19]	VSS[99]	AM26
AB5	VSS[20]	VSS[100]	AM28
AB8	VSS[21]	VSS[101]	BA42
AC2	VSS[22]	VSS[102]	AM30
AC52	VSS[23]	VSS[103]	AM31
M8	VSS[24]	VSS[104]	AM32
AD12	VSS[25]	VSS[105]	AM34
AD16	VSS[26]	VSS[106]	AM35
AD23	VSS[27]	VSS[107]	AM38
AD30	VSS[28]	VSS[108]	AM39
P30	VSS[29]	VSS[109]	AM42
AD32	VSS[30]	VSS[110]	AM46
AD34	VSS[31]	VSS[111]	AV22
AD42	VSS[32]	VSS[112]	AM49
AD46	VSS[33]	VSS[113]	AM7
AD49	VSS[34]	VSS[114]	AA50
VSS[35]	VSS[115]	VSS[116]	BB10
AD7	VSS[36]	VSS[117]	AN32
AE2	VSS[37]	VSS[118]	AN50
AE4	VSS[38]	VSS[119]	AN52
AE12	VSS[39]	VSS[120]	AP12
Y13	VSS[40]	VSS[121]	AP42
AH49	VSS[41]	VSS[122]	AP46
AI4	VSS[42]	VSS[123]	AP49
AF35	VSS[43]	VSS[124]	AP5
AF13	VSS[44]	VSS[125]	AP8
AF45	VSS[45]	VSS[126]	AR2
AF46	VSS[46]	VSS[127]	AR52
AF49	VSS[47]	VSS[128]	AT11
AF5	VSS[48]	VSS[129]	BA12
AF8	VSS[49]	VSS[130]	AI48
Y20	VSS[50]	VSS[131]	AT32
AG52	VSS[51]	VSS[132]	AT36
V30	VSS[52]	VSS[133]	AT41
AH11	VSS[53]	VSS[134]	AT47
AH15	VSS[54]	VSS[135]	AT7
AH16	VSS[55]	VSS[136]	AV12
AH24	VSS[56]	VSS[137]	AV16
V35	VSS[57]	VSS[138]	AV20
AH32	VSS[58]	VSS[139]	AV24
AV18	VSS[59]	VSS[140]	AV30
AH43	VSS[60]	VSS[141]	AV34
AH47	VSS[61]	VSS[142]	AV38
AH7	VSS[62]	VSS[143]	AV42
AI19	VSS[63]	VSS[144]	AV46
AJ2	VSS[64]	VSS[145]	AV49
AJ20	VSS[65]	VSS[146]	AV5
AJ22	VSS[66]	VSS[147]	AV8
AJ23	VSS[67]	VSS[148]	AW14
AJ26	VSS[68]	VSS[149]	AW18
AJ28	VSS[69]	VSS[150]	AW2
AJ32	VSS[70]	VSS[151]	BE9
Y11	VSS[71]	VSS[152]	AW32
AT5	VSS[72]	VSS[153]	AW36
AI4	VSS[73]	VSS[154]	AW40
Y19	VSS[74]	VSS[155]	AW52
AK12	VSS[75]	VSS[156]	AY11
AM41	VSS[76]	VSS[157]	AY43
AN19	VSS[77]	VSS[158]	AY47
AK26	VSS[78]		
Y32	VSS[79]		
AK22			
AK23			
AK28			

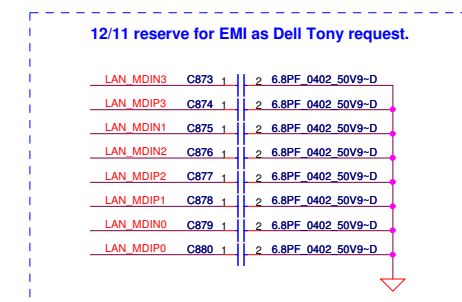
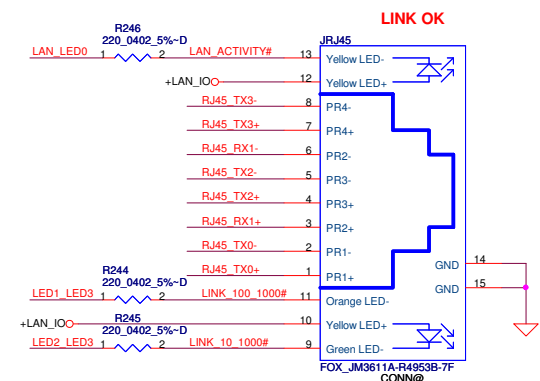
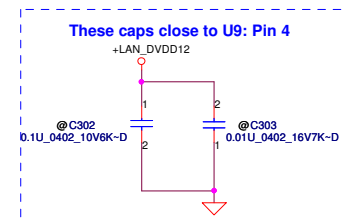
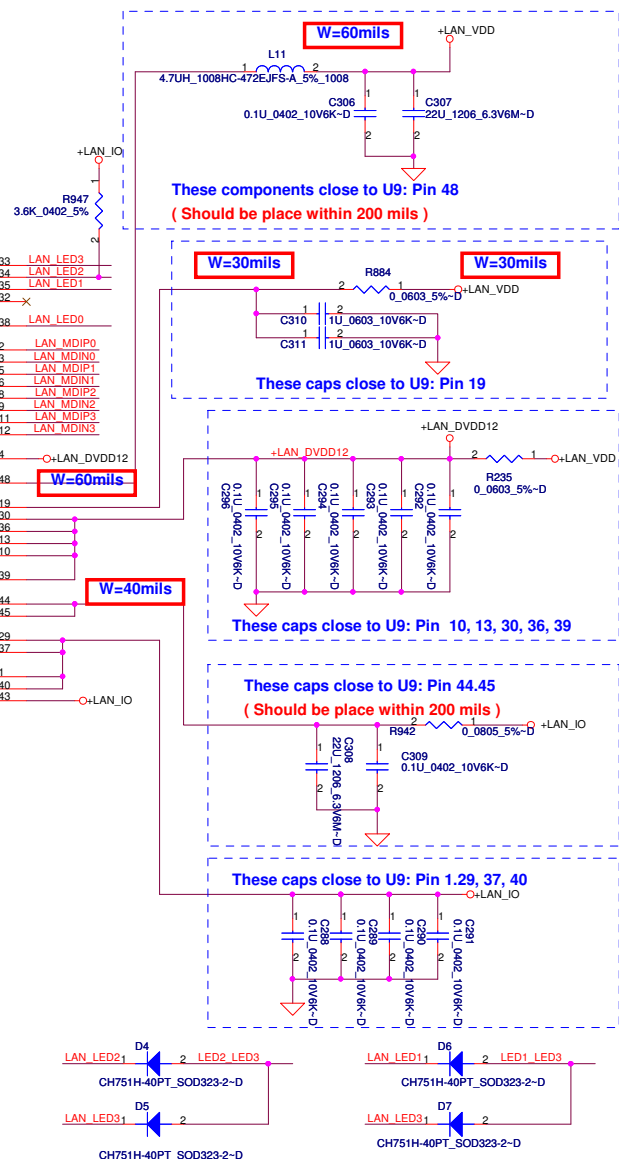
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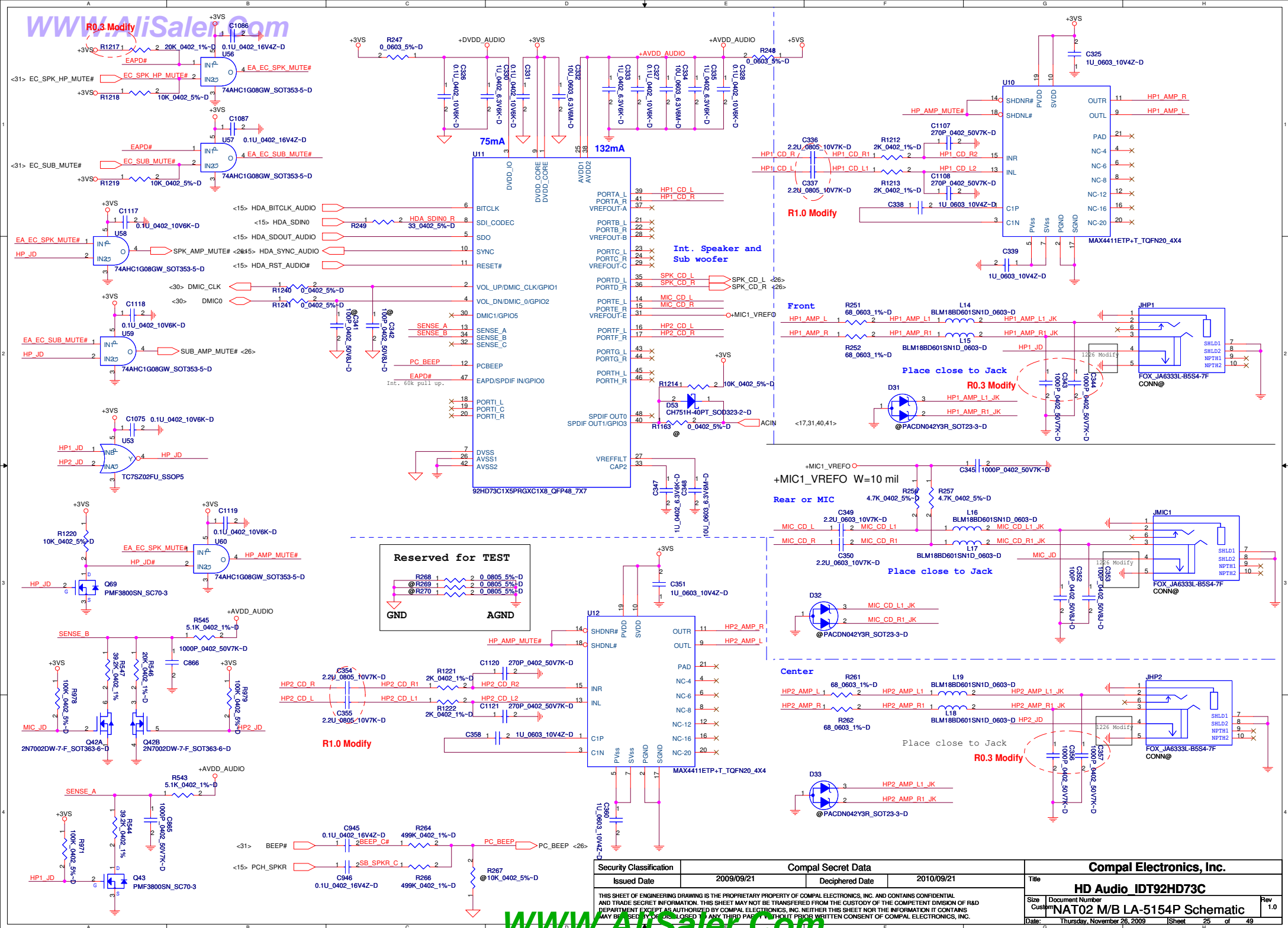
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								PCH (9/9) VSS			
Size		Document Number		Customer		Rev					
								NAT02 M/B LA-5154P Schematic			
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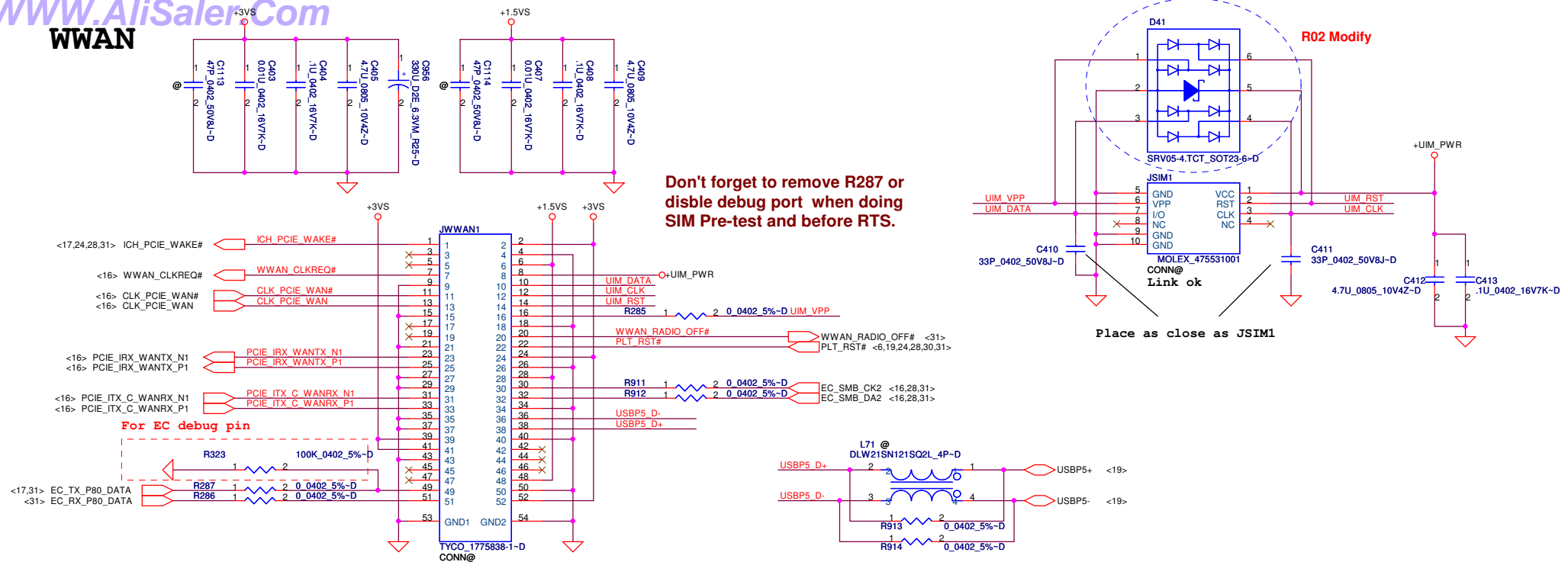


LEDS1-0	0 0	0 1	1 0	1 1
LED0	Tx / Rx	Tx / Rx	Tx	LINK10 / ACT
LED1	LINK100	LINK10 / 100 / 1000	LINK	LINK100 / ACT
LED2	LINK10	LINK10 / 100	Rx	FULL
LED3	LINK1000	LINK1000	FULL	LINK1000 / ACT

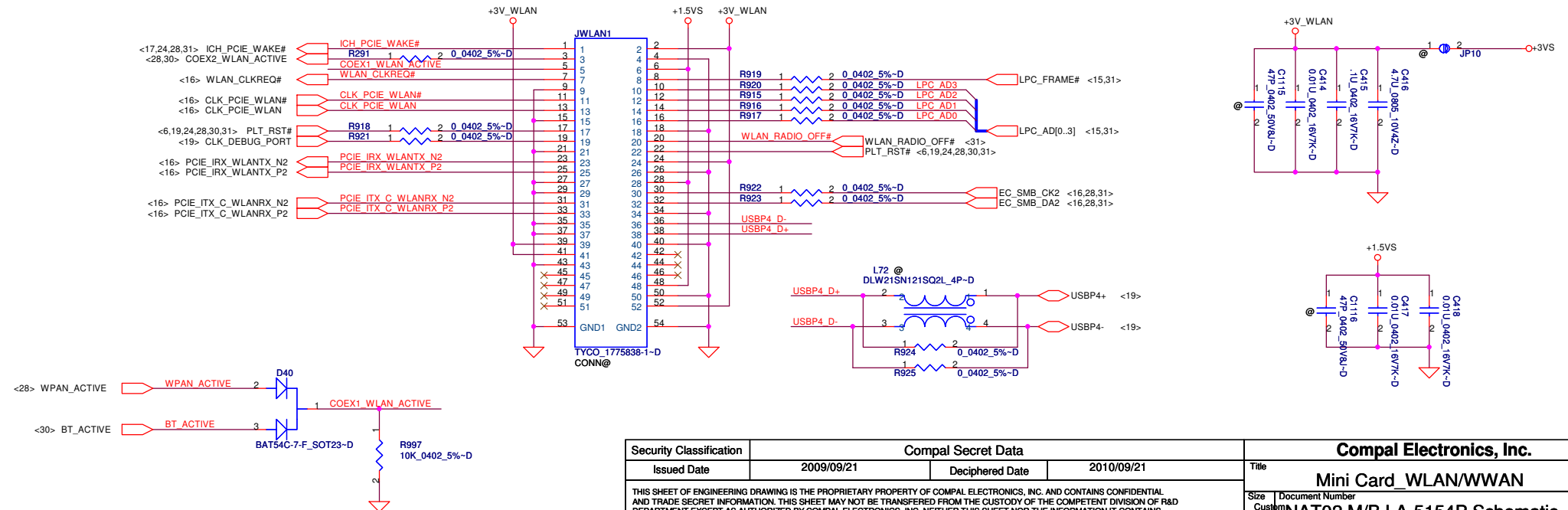




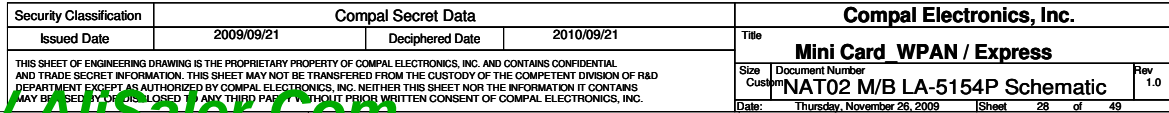




## WLAN



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Size	Document Number	Customer		Rev	
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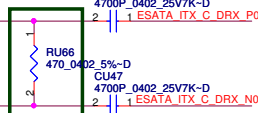
JSATA1

Place close U67 pin 21 & pin22

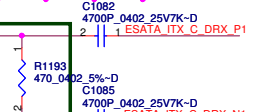


### Output De-emphasis Adjustment

SEL3_ [A:B]	De-emphasis
0	0dB
1	-3.5dB

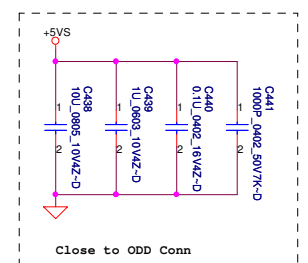
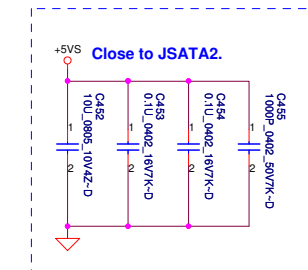


**Place close U55 pin 21 & pin22**



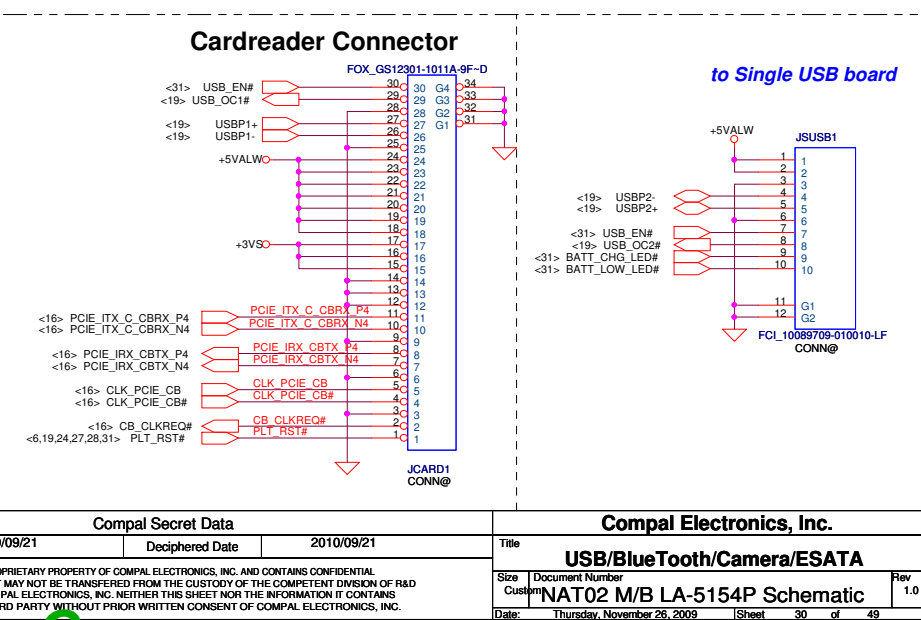
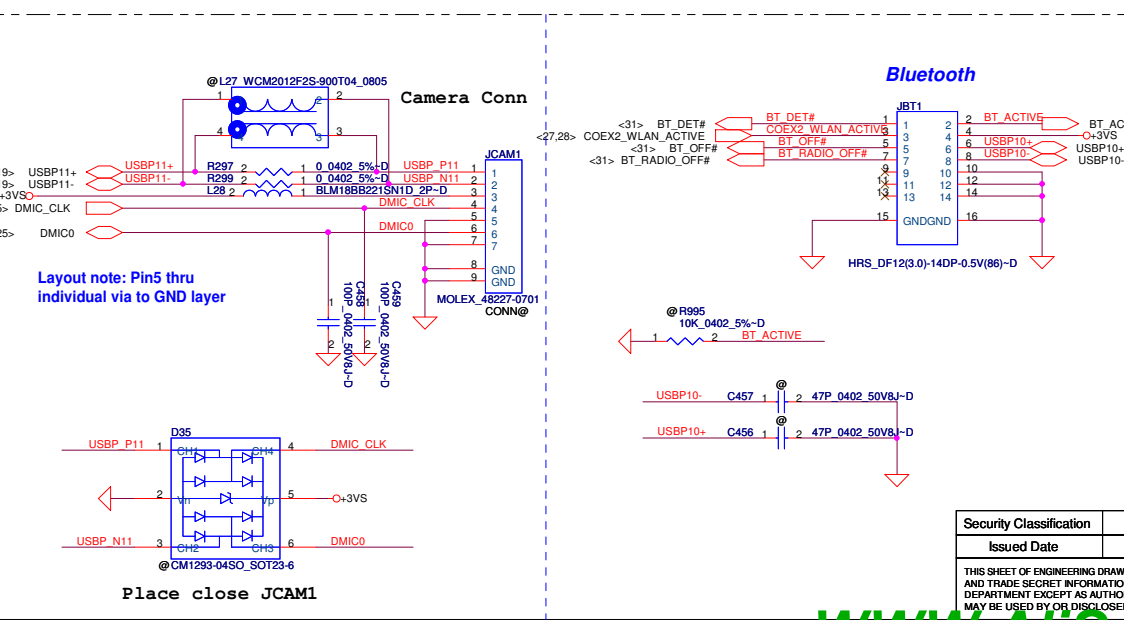
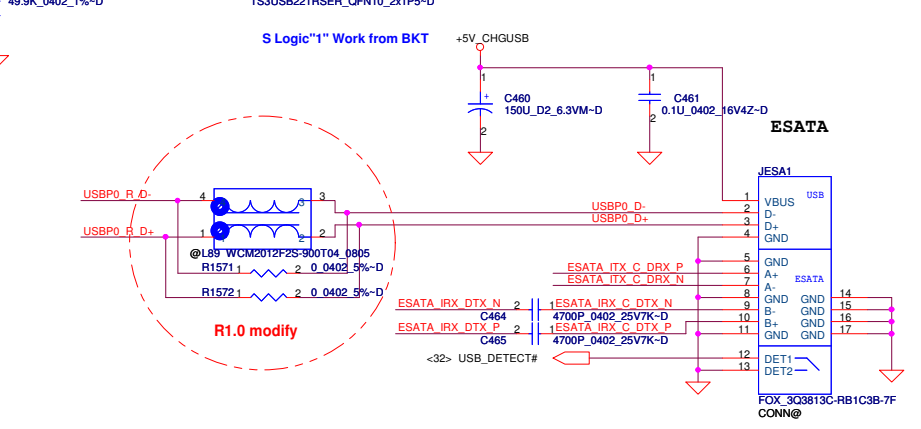
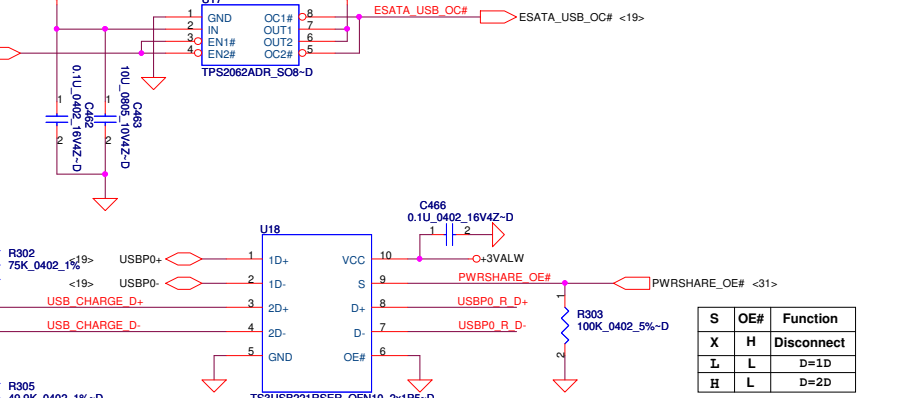
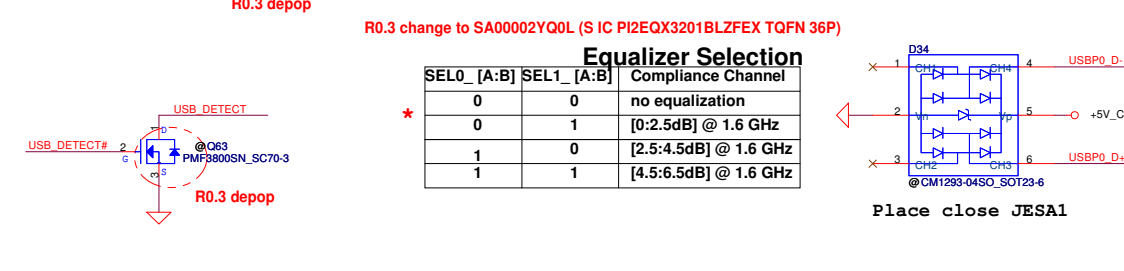
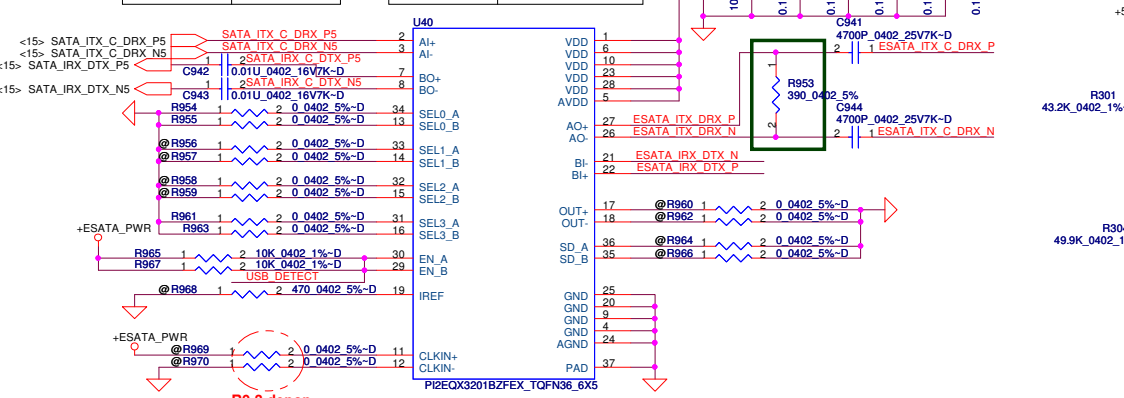
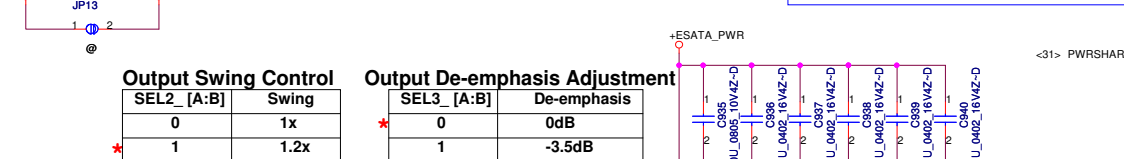
**R0.3 change to SA00002YQ0L (S IC PI2EQX3201BLZFEX TQFN 36P)**

## MOLEX\_47662-2000



<b>Compal Electronics, Inc.</b>			
Title		<b>ODD/SATA HDD</b>	
Size	Document Number		Rev
Custom	<b>NAT02 M/B LA-5154P Schematic</b>		<b>1.0</b>
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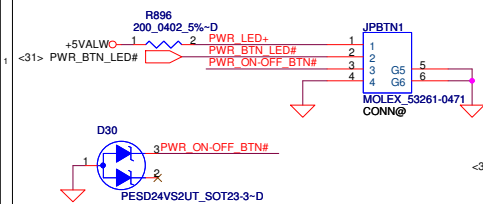




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				Customer NAT02 M/B LA-5154P Schematic	1.0
Date: Thursday, November 26, 2009		Sheet 30 of 49			

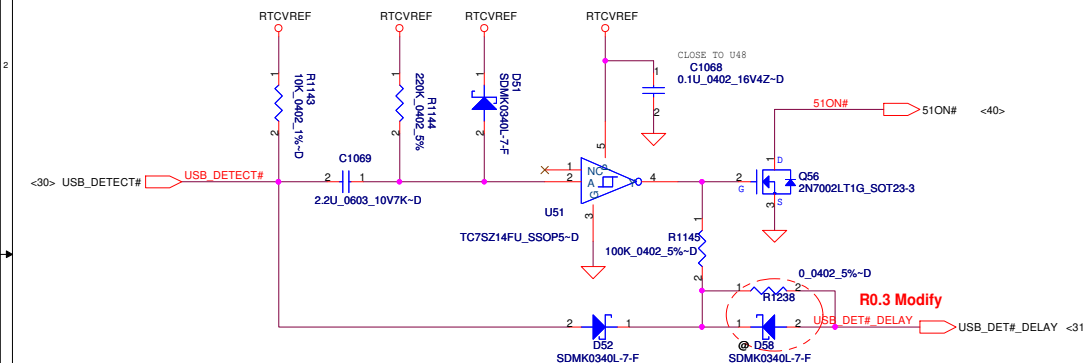
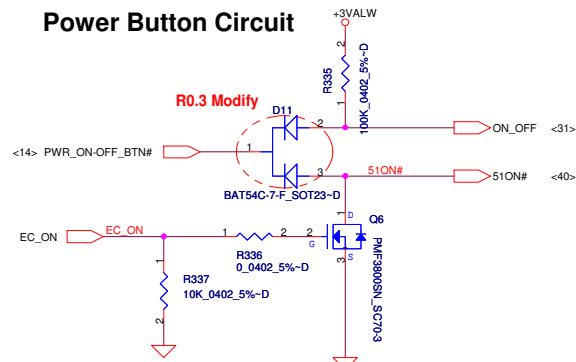


**To power board**

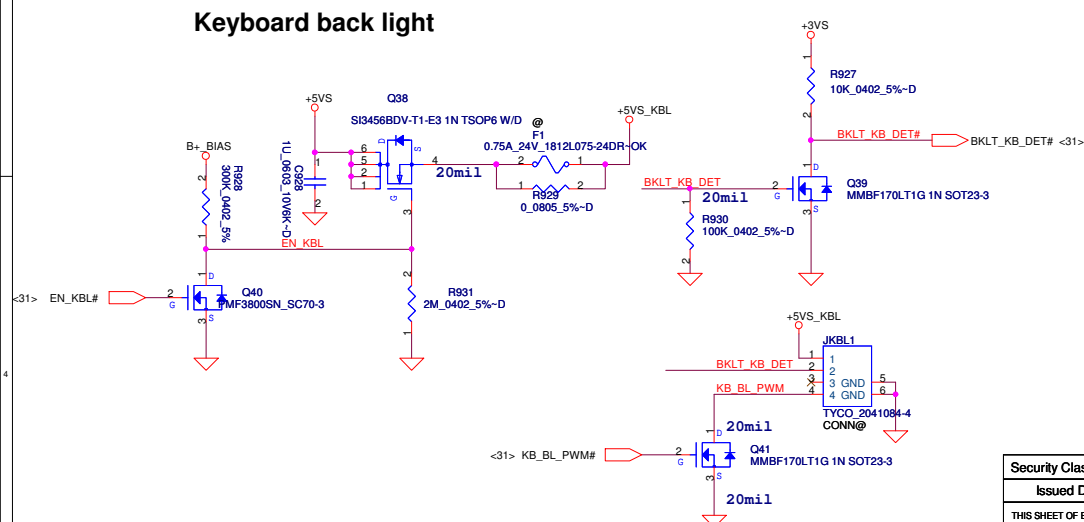


**Place close JPBTN1**

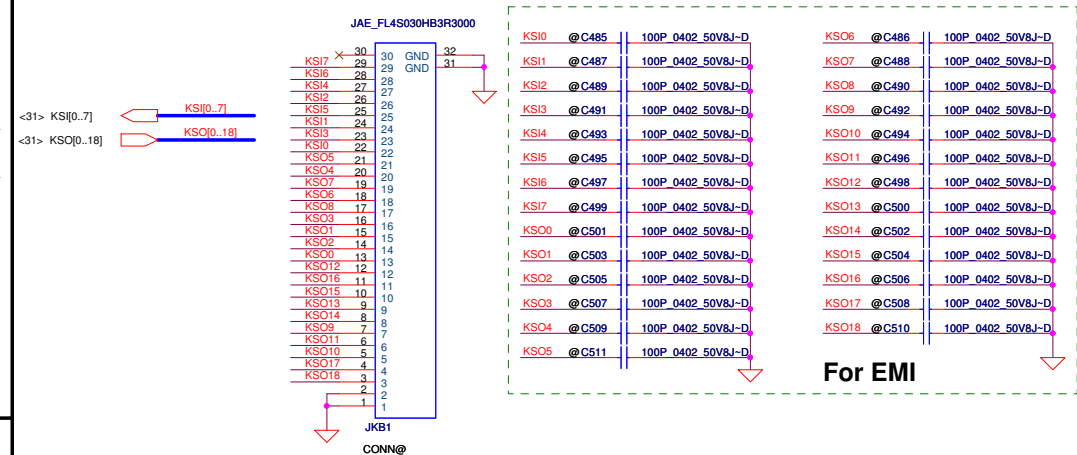
## Power Button Circuit



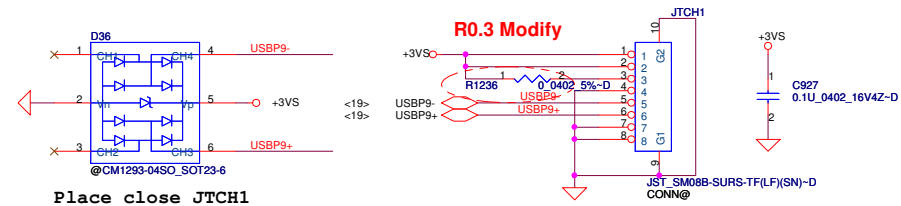
## Power share



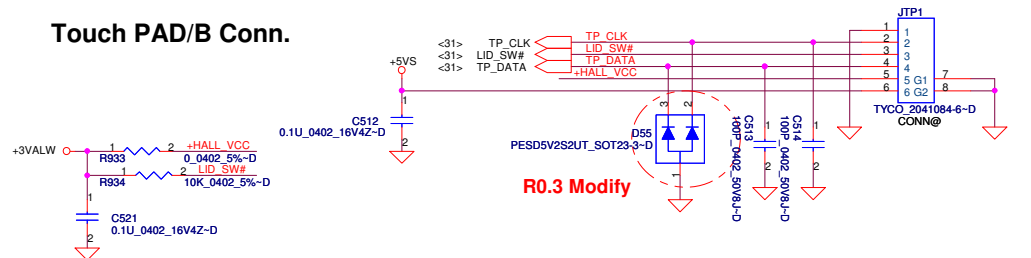
**INT\_KB\_Conn.1**



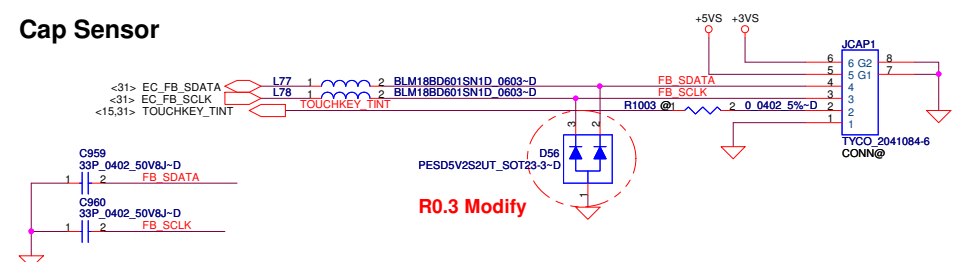
## Touch Screen Connector



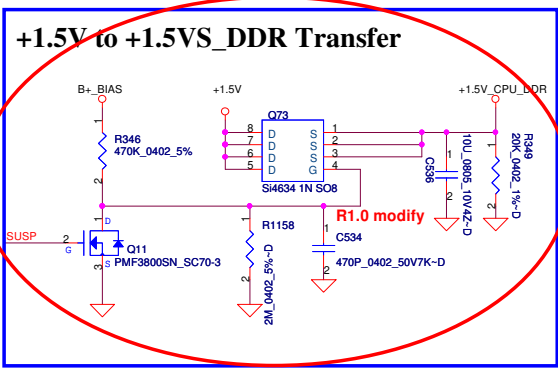
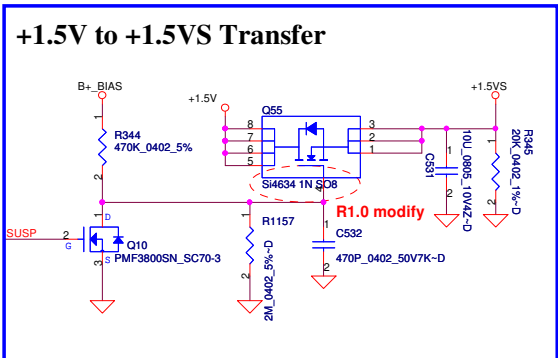
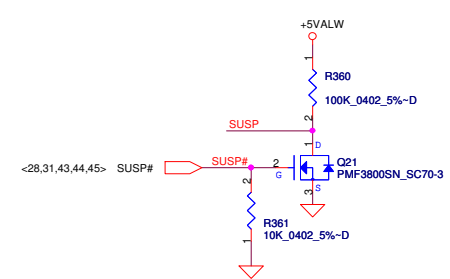
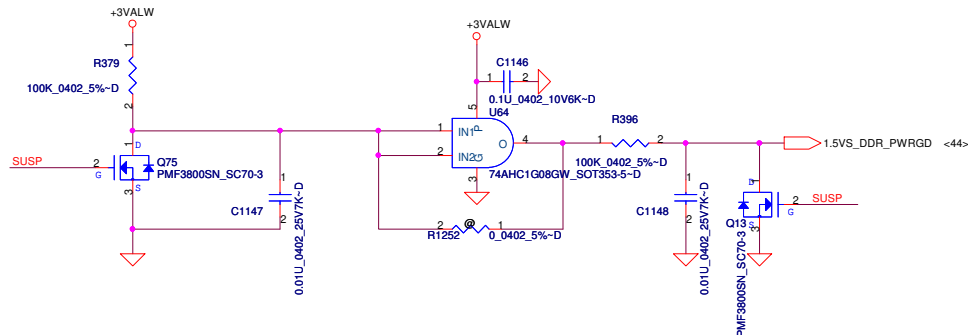
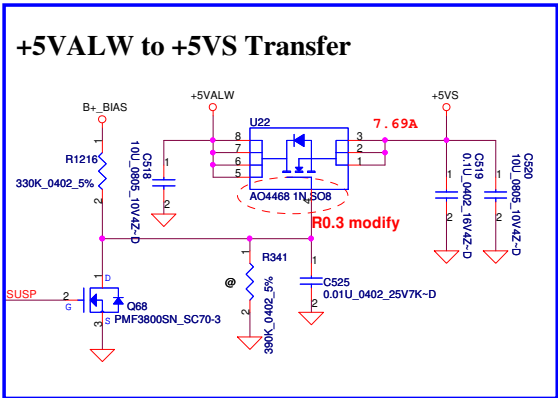
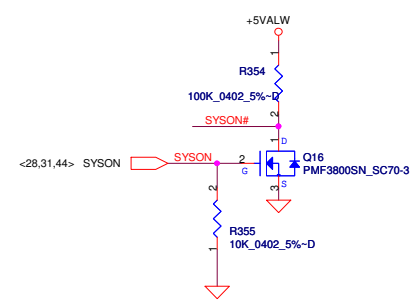
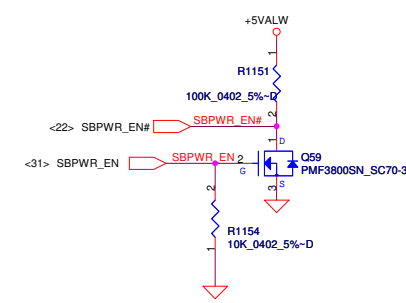
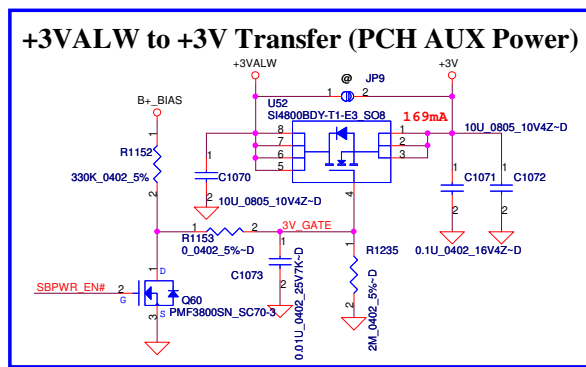
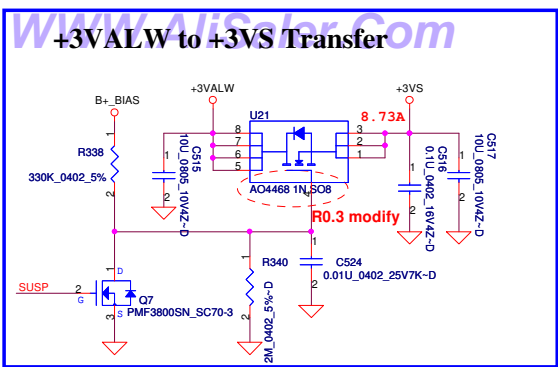
### Touch PAD/B Conn.



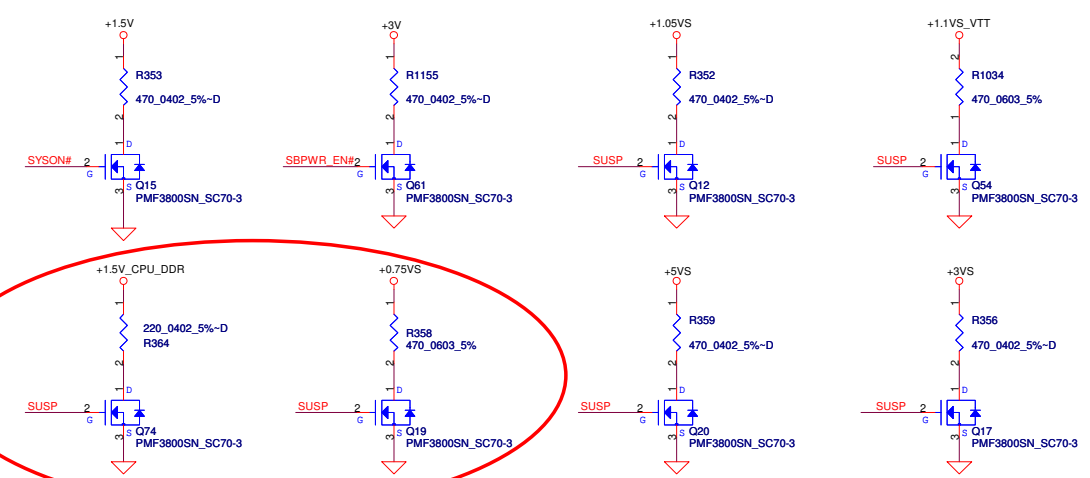
## Cap Sensor



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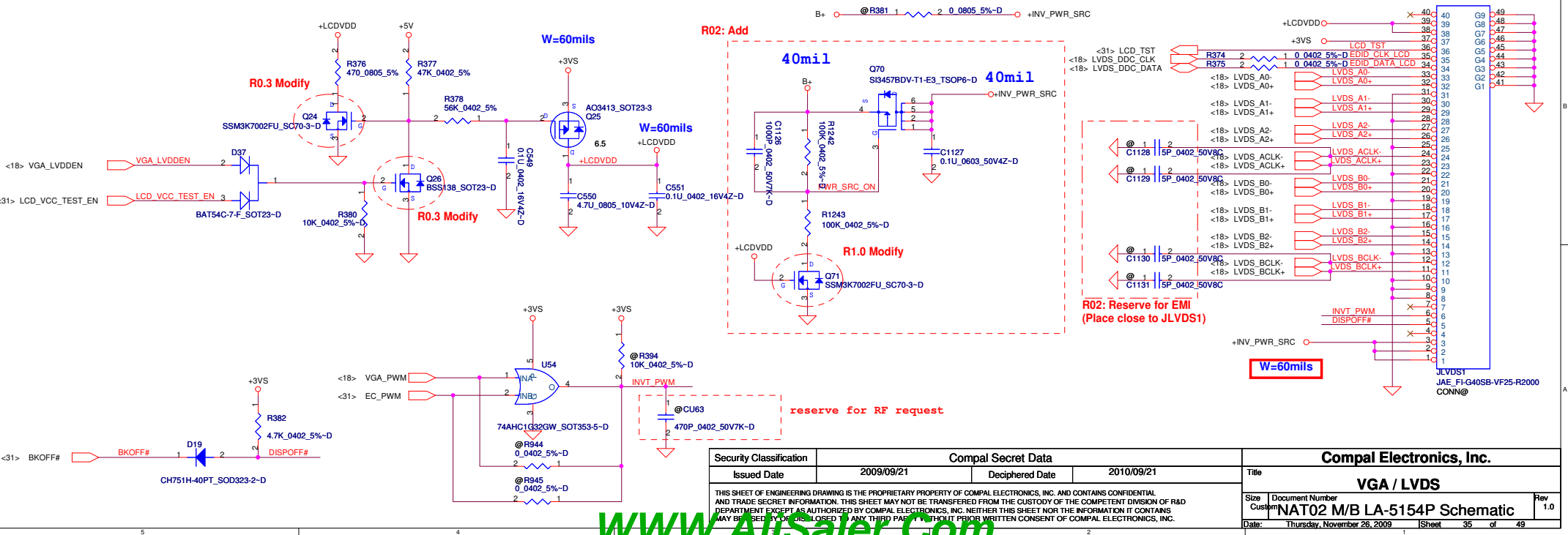
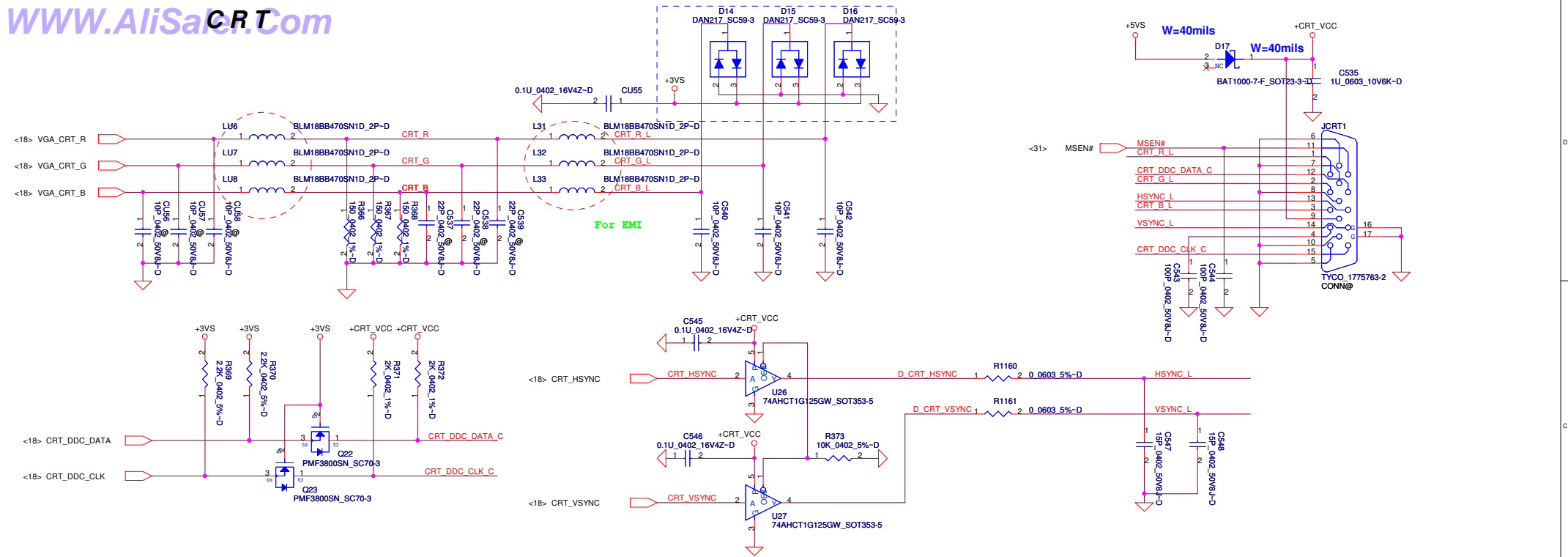
### Discharge Circuit

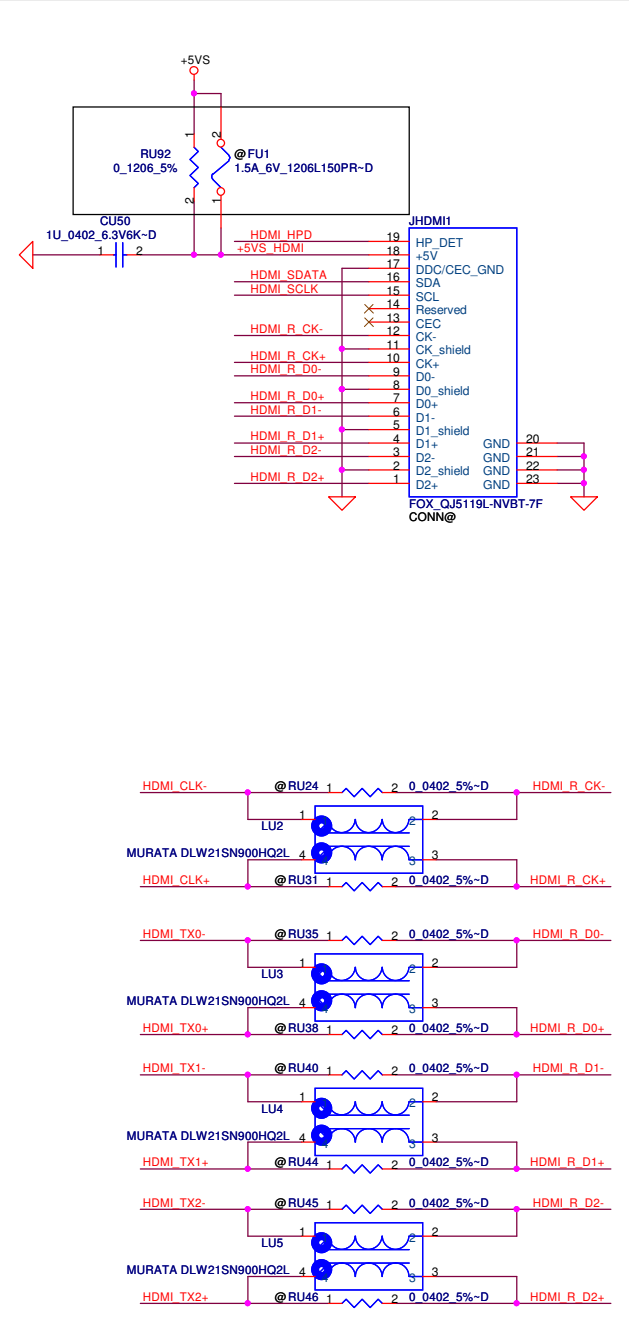
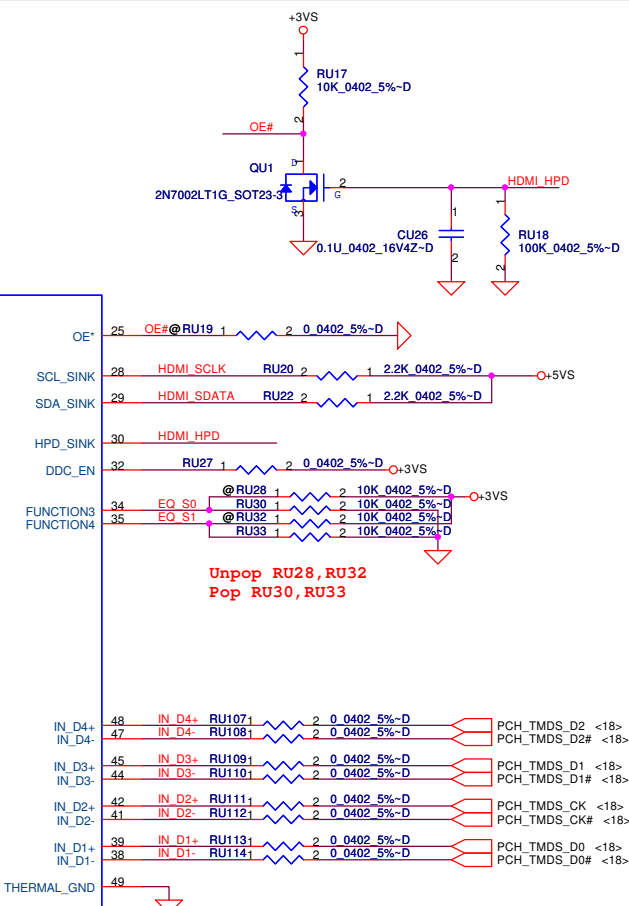


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Size			Document Number		Customer		Rev	
			NAT02 M/B LA-5154P Schematic				1.0	
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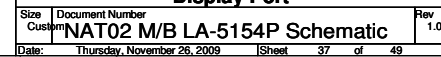
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Issued Date	2009/09/21	Deciphered Date	2010/09/21	Title	
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				Size Custom	Document Number NAT02 M/B LA-5154P Schematic Rev 1.0
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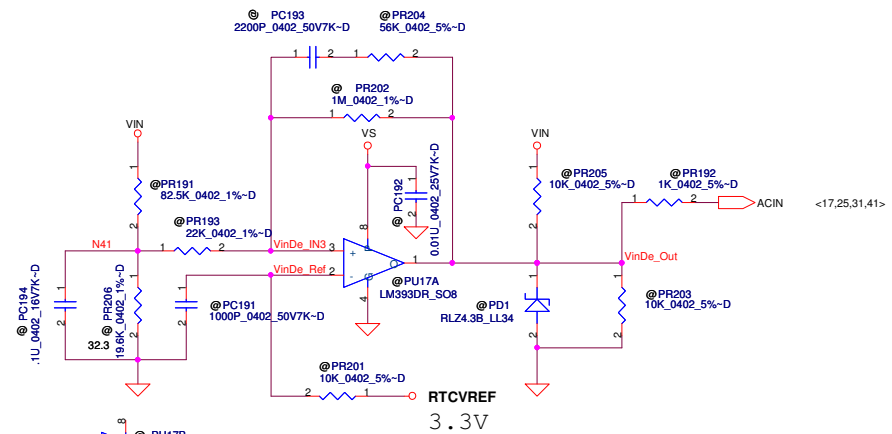
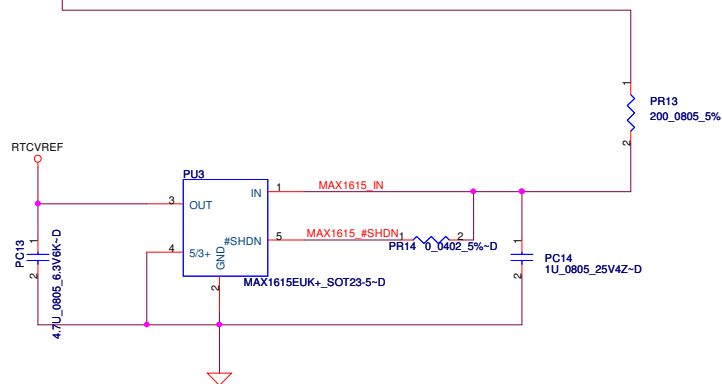
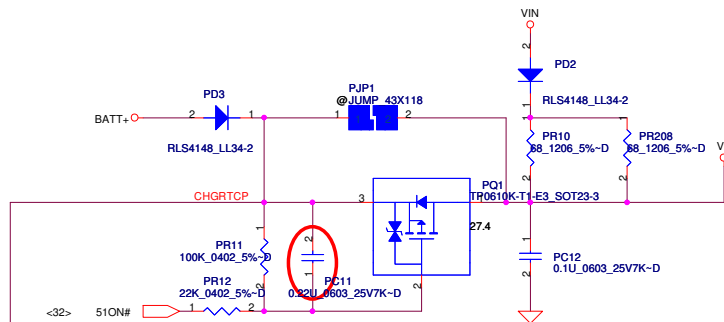
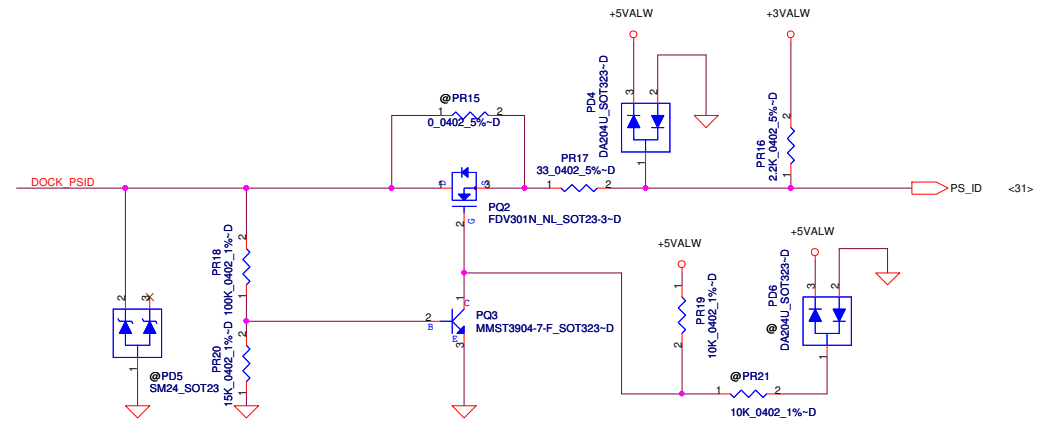
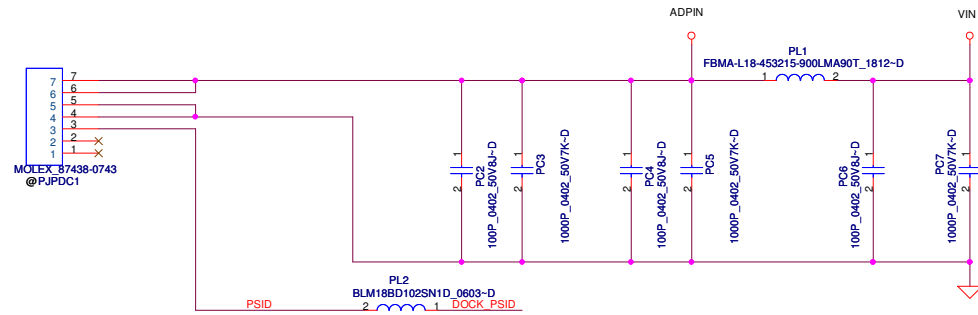


Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
01	08	PROCESSOR (4/6) PWR,Bypass	2009/07/22	Compal	Design change for IMVP6.5 current gain	R343 un-pop,R1072 pop,R1074 pop,R1073 unpop,R1075 pop,R1076 un-pop	Rev02 (X01)
02	16	PCH (2/9) PCIE, SMBUS, CLK	2009/08/10	Compal	XTAL25_IN should be pulled to GND using a 0Q resistor.	Pop C1027 with a 0 ohm resister	Rev02 (X01)
03	06	+1.5V and reseret	2009/08/10	Compal	Reduce S3 state Power	Add U63,Q36,R290,R310,R1103,R1136,C1142	Rev02 (X01)
04	09	Separate +1.5V power	2009/08/10	Compal	Reduce S3 state Power	Add PJP12,PJP13,PJP14,C1033,C1143,C1144,C1145	Rev02 (X01)
05	11	VrefDQ	2009/08/10	Compal	VrefDQ should be Maintained within SPEC during S3	Add Q37,R313	Rev02 (X01)
06	12	VrefDQ	2009/08/10	Compal	VrefDQ should be Maintained within SPEC during S3	Add Q44,R321	Rev02 (X01)
07	20	DDR_RST_GATE	2009/08/10	Compal	DDR_RST_GATE from GPIO46	Add GPIO46	Rev02 (X01)
08	33	VDDQ	2009/08/10	Compal	Processor VDDQ should be turned off in S3	Add Q73,Q11,R346,R349,R1158,C534,C536	Rev02 (X01)
09	15	GPIO1D	2009/08/18	Compal	ADD EC GPIO1D to PCH GPIO33	Add GPIO33	Rev02 (X01)
10	26	Sub woofer / Speaker AMP	2009/08/25	Compal	Reserve Subwoofer delay circuit for mute	Reserve D60, R1568, C1558	Rev02 (X01)
11	24,31		2009/08/25	Compal	Follow crystal vendor's recommend	C318,C319,C479,C481 BOM change to SE071330J8L (S CER CAP 33P 50V +-5% NPO 0402)	Rev02 (X01)
12	6	PROCESSOR (2/6) CLK,JTAG	2009/08/25	Compal	Intel S3 solution disable	POP R1054,R1055,R1121; Depop Q36,R290,R1103	Rev02 (X01)
13	11,12		2009/08/25	Compal	Intel S3 solution disable	Depop Q37,Q44,R56	Rev02 (X01)
14	33	DC/DC Interface	2009/08/25	Compal	Intel S3 solution disable	Depop Q73 Q73 BOM change to SB000001Y8L R358 BOM change to SD013470080 (S RES 1/10W 470 +-5% 0603)	Rev02 (X01)
15	35	VGA / LVDS	2009/08/25	Compal	to improve +LCDVDD voltage quality.	Pop C550	Rev02 (X01)
16	31	EC_KB926/BIOS/Reed SW	2009/08/25	Compal	Board ID change	R312 BOM change to 8.2K	Rev02 (X01)
17	31	EC_KB926/BIOS/Reed SW	2009/08/25	Compal	Reserve a 0ohm resister on SUS_PWR_ACK from PCH to EC	Reserve R1569 on SUS_PWR_ACK	Rev02 (X01)
18	35		2009/09/01	Compal	In common with MV	Q71 BOM change to SB00000960L	Rev02 (X01)
19	20		2009/09/21	Compal	S3 POWER Reduction update from Intel	Reserve C1559 on DDR_RST_GATE	Rev03 (X02)
20	30		2009/09/21	Compal	Reserve common choke on USB0	Reserve L89,R1571,R1572	Rev03 (X02)

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01	30	ESATA	2009/09/21	Compal	Add ESATA re-driver	Unpop R1164,R1165,R1166,R1167,R949,R950,R951,R952 pop JP13,C941,C944,C942,C943	Rev03 (X02)
02	33		2009/09/21	Compal		Q55,Q73 BOM change to SB00000DA0L	Rev03 (X02)
03	8		2009/09/21	Compal	BOM reason	C1009,C1010 BOM change to SGA00002U1L	Rev03 (X02)
04	25	HD Audio_IDT92HD73C	2009/09/21	Compal		BOM change for C336,C337,C354,C355	Rev03 (X02)
05	26	Sub woofer/Speaker AMP	2009/09/21	Compal	implement Subwoofer delay circuit for mute	Pop D60,C1558 R1568 change to 330K	Rev03 (X02)
06	37	Display Port	2009/09/21	Compal	Follow Intel HPD design	remove RU98,RU99;add RU115; change QU4 to SB50138008L	Rev03 (X02)
07	22	PCH (8/9) PWR	2009/09/21	Compal	Follow Intel design	Add LU9,CU64,CU65,CU66	Rev03 (X02)
08	11, 12	DDRIII SO-DIMM	2009/10/08	Compal	Add M3 solution	Pop R166,R178	Rev03 (X02)
09	31	EC	2009/10/12	Compal	For ENE issue	Add R322 47K ohm pull high	Rev03 (X02)
10	17	PCH	2009/10/15	Compal	Bom reason	Change U2 SA007080B90 to SA007080100	Rev03 (X02)
11	19	PCH	2009/10/16	Compal	Design follow NAT01	Change R1139 SD02810038L to SE000000K8L	Rev03 (X02)
12	31	EC	2009/10/22	Compal	Change Board ID	Change R312 SD02882018L to SD02818028L	Rev03 (X02)
13	32	Touch Screen	2009/11/02	Compal	Add for Touch Screen issue	Add R1236	Rev03 (X02)
14	36	HDMI	2009/11/24	Compal	For HDMI Deep color mode	Unpop RU28,RU32 , Pop RU30,RU33	Rev10 (A00)
15	30	Camera	2009/11/02	Compal	improve 888MHZ noise	Add C458,C459	Rev10 (A00)
16	27	WLAN	2009/11/05	Compal	for EC debug pin	Add R323 100K pull down	Rev10 (A00)
17	9	PWR	2009/11/05	Compal	Intel suggest to reduce GFX voltage overshoot	Change RU93 to 470ohm	Rev10 (A00)
18	26	Sub woofer/Speaker Amp	2009/11/16	Compal	Follow NAT01 design	Change C922 & C1102 to 0.1uF	Rev10 (A00)
19	31	EC	2009/11/19	Compal	Change Board ID	Change R312 SD02818028L to SD02833028L	Rev10 (A00)
20	36	HDMI	2009/11/24	Compal	Change HDMI level shift	Change U63 to SA00003GT00	Rev10 (A00)
21	36	HDMI	2009/11/24	Compal	For HDMI Deep color mode	Add RU25 4.7K pull down for ASM1442	Rev10 (A00)
22	36	HDMI	2009/11/24	Compal	For HDMI Deep color mode	Reserve RU37 4.7K pull high for ASM1442	Rev10 (A00)
23	36	HDMI	2009/11/24	Compal	For HDMI Level shift ASM1442	Change RU29 to 3.4K for ASM1442	Rev10 (A00)
24							
25							

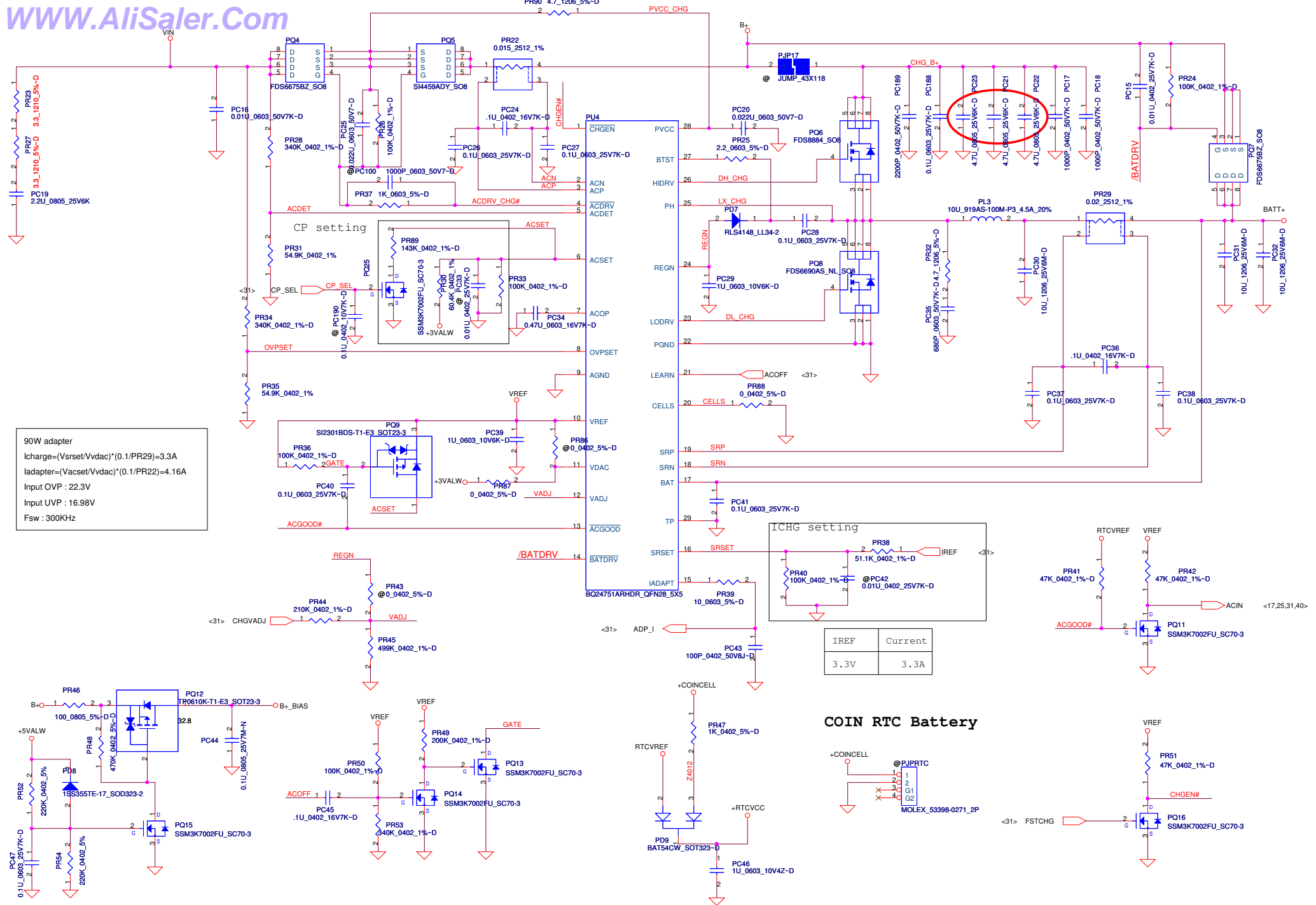
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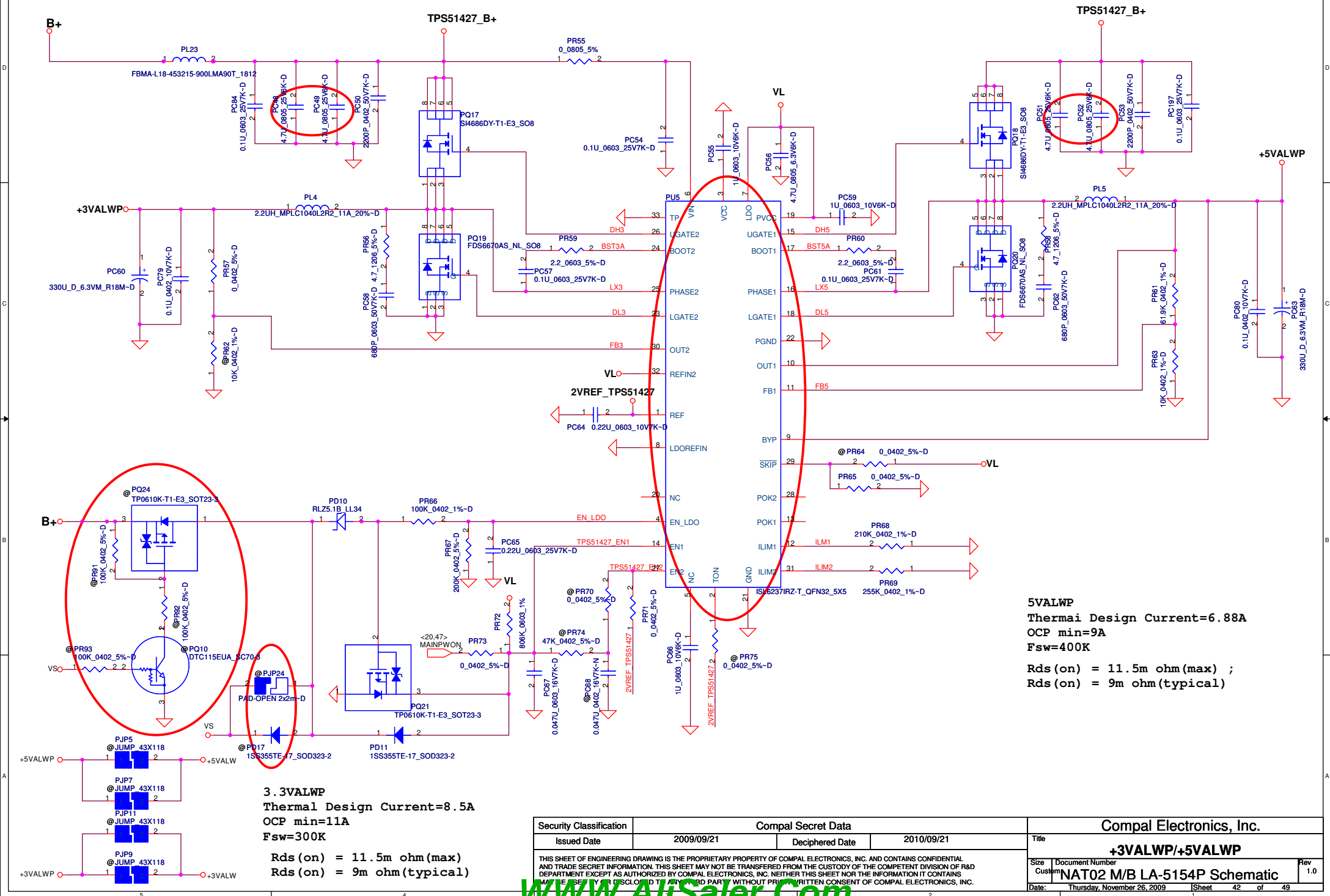
# Vin Detector

	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813

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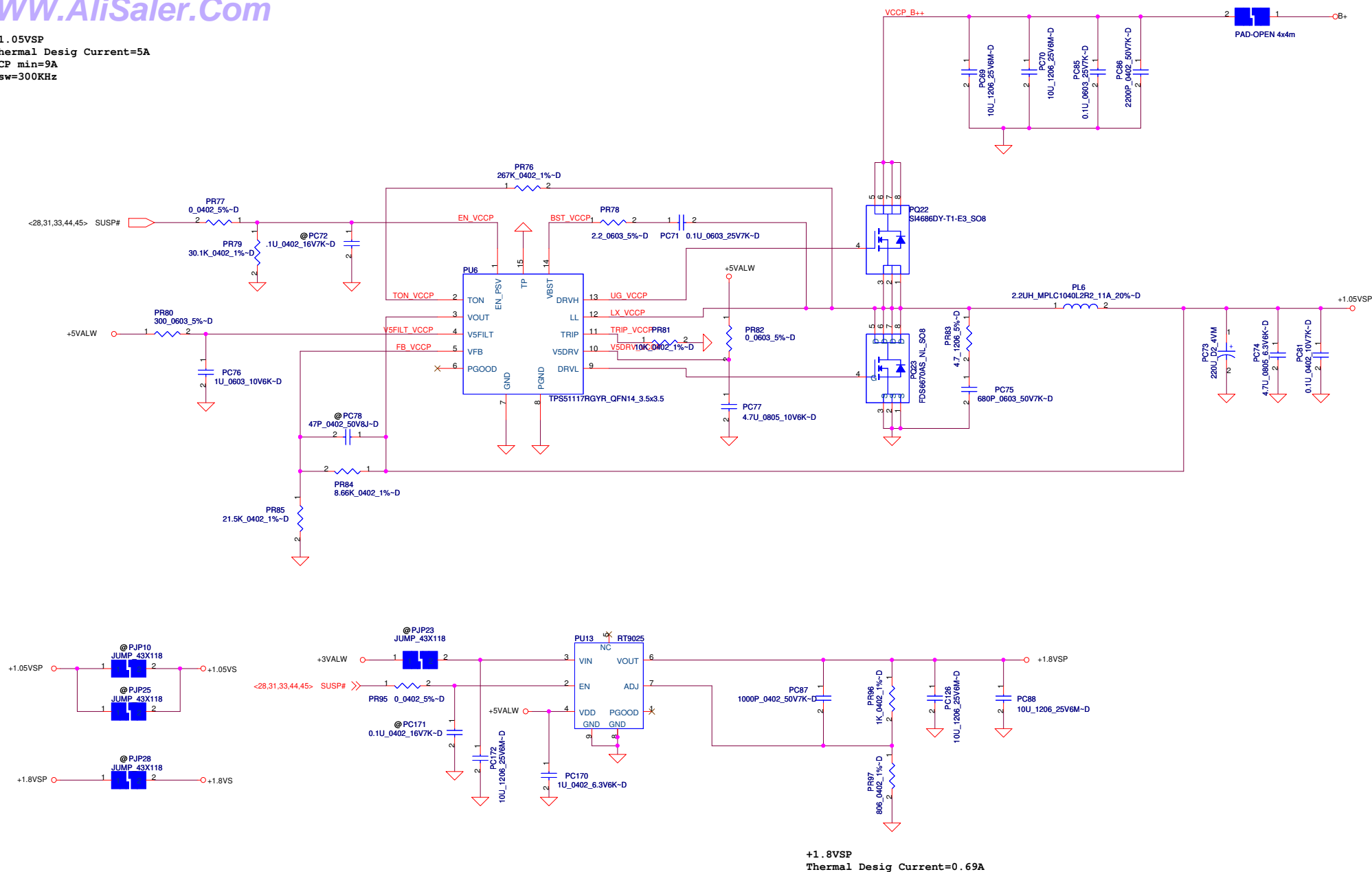


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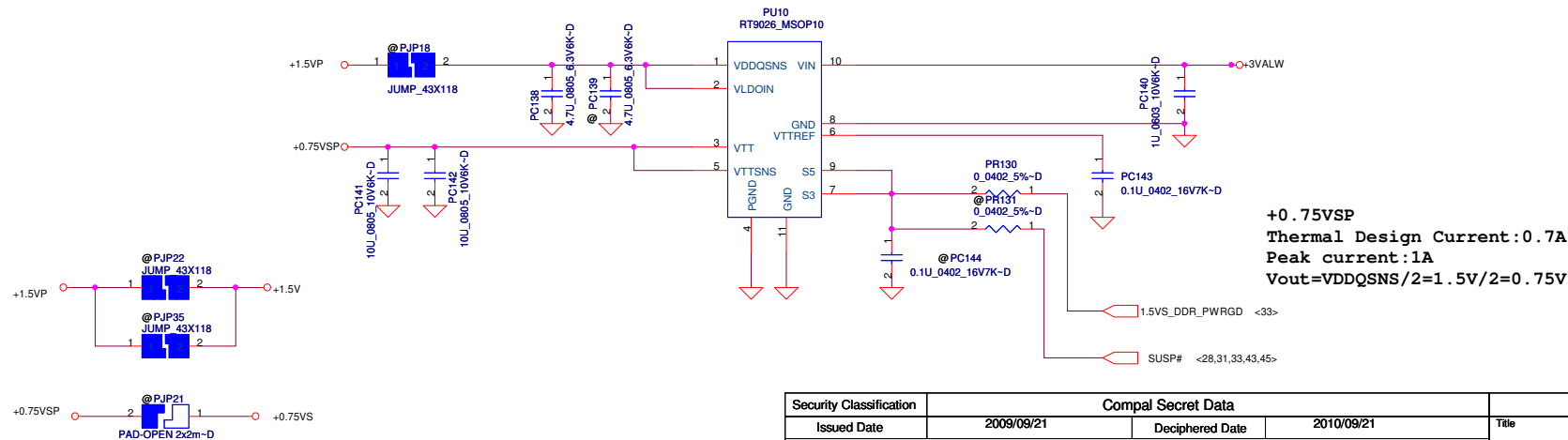
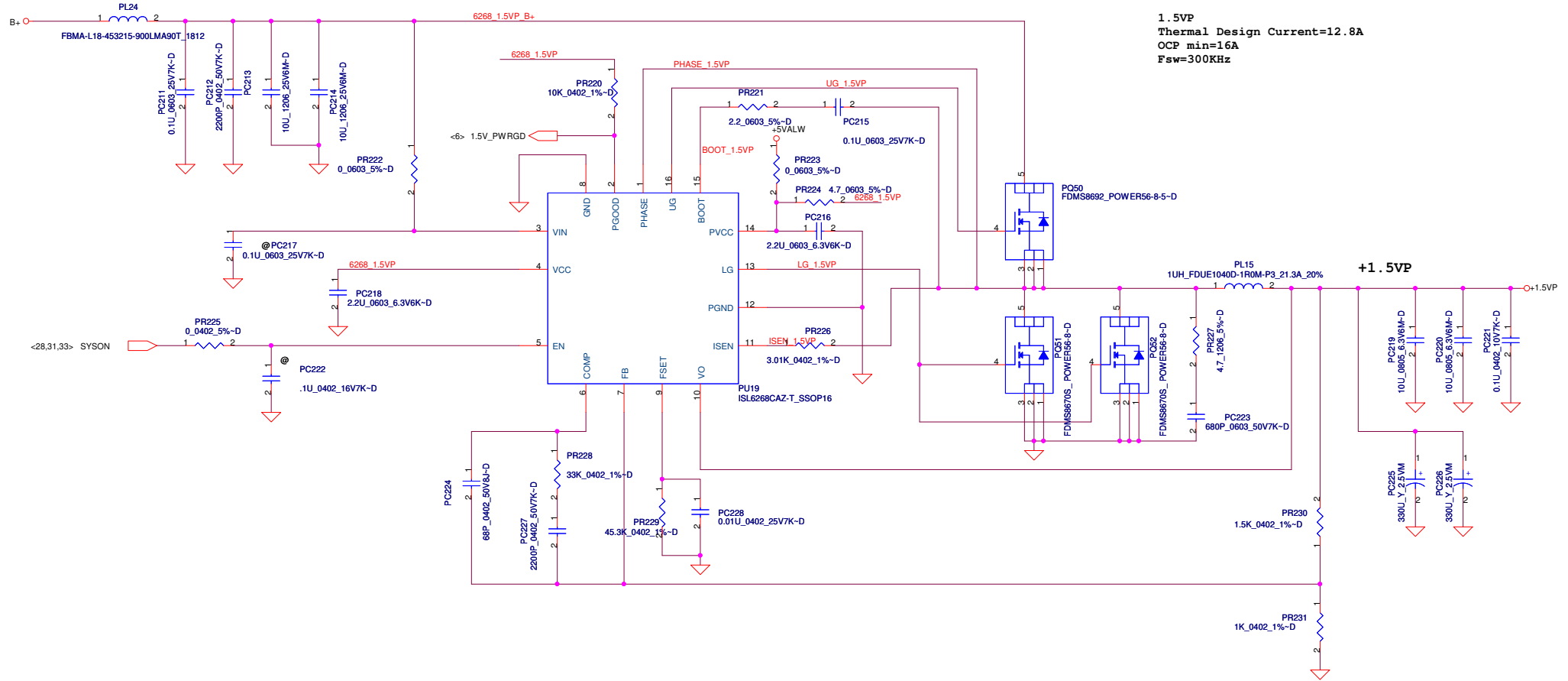


+1.05VSP  
Thermal Desig Current=5A  
OCP min=9A  
Fsw=300KHz

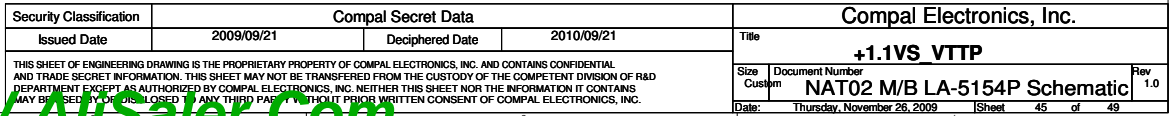


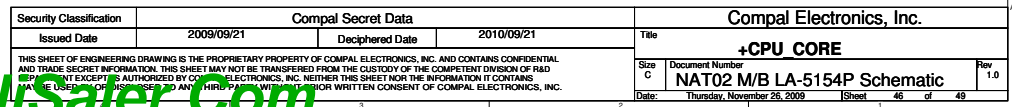
+1.8VSP  
Thermal Desig Current=0.69A

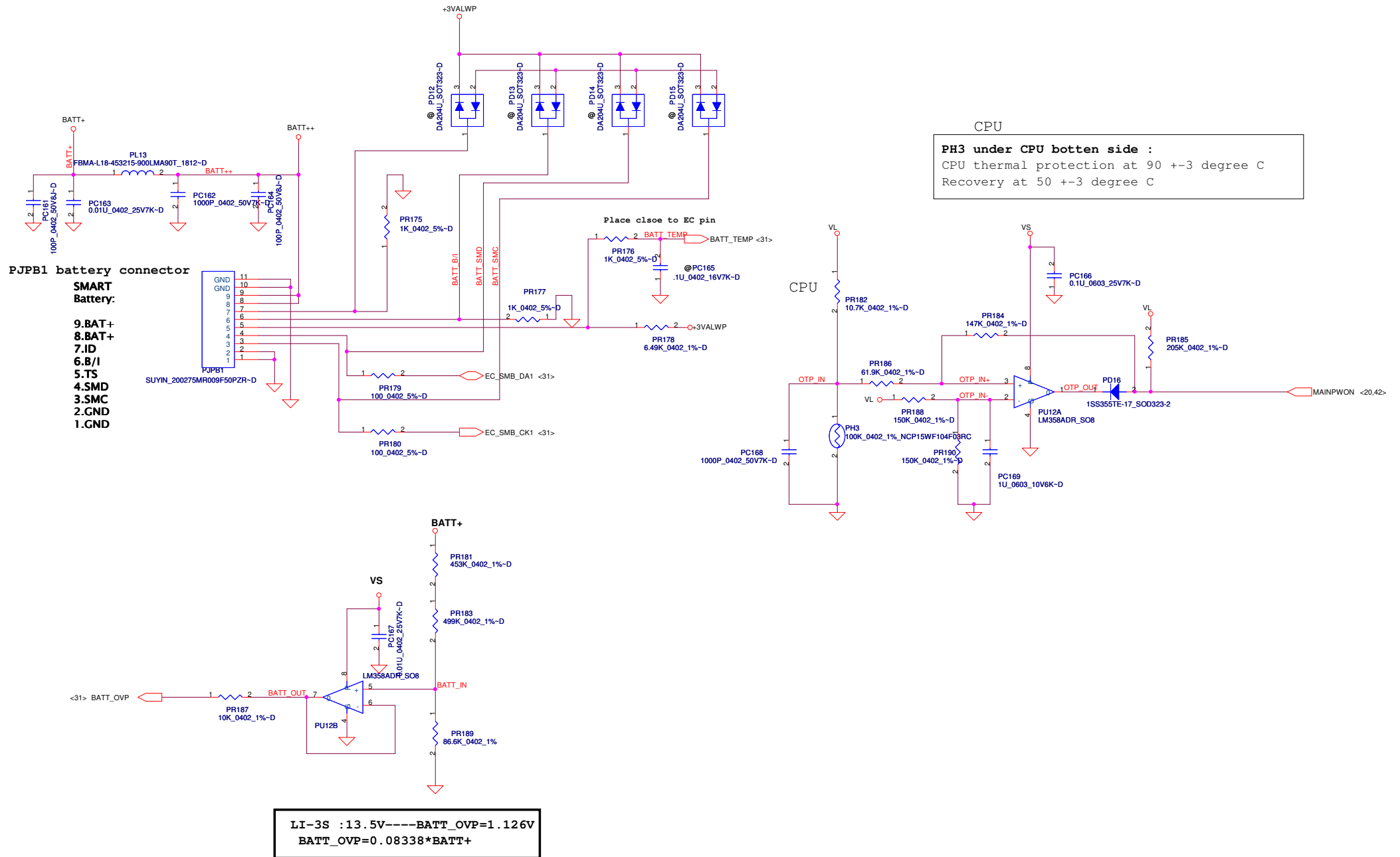
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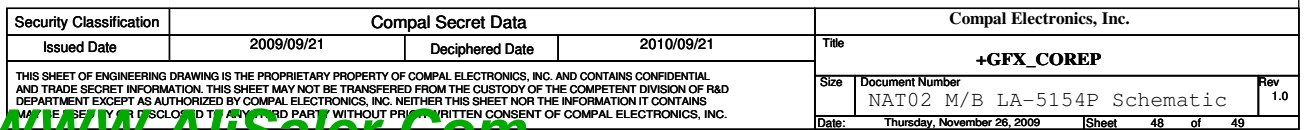






CPU  
 PH3 under CPU botten side :  
 CPU thermal protection at 90 +-3 degree C  
 Recovery at 50 +-3 degree C

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01	P51	+1.1VS_VTTP	08/31	Lin Will	Slove EMI	Add PL22 and Change PR233 form 0 ohm to 2.2 ohm.	X01
02	P53	+CPU_Core	08/31	Lin Will	Slove EMI	Change PR259 and PR291 from 0 ohm to 2.2 ohm.	X01
03	P55	+GFX_COREP	08/31	Lin Will	Adjust load-line and Imon	Change PR306 form 9.54K to 9.76K, PR210 from 22.3K to 24K and PC296 from 0.22uF to 0.033uF.	X01
04	P55	+GFX_COREP	08/31	Lin Will	Adjust output voltage ripple and thermal disipation	Add PQ46 and change PC289, PC290, PC301 from 4.7uF to 10uF, and PC105, PC106 from 330uF 9mohm to 330uF 6mohm.	X01
05	P43	+1.05VSP/+0.75VSP	10/14	Lin Will	Slove the IC input power sequence.	Change TPS51117 V5FILT and V5DRV from 5VS to 5VALW.	X02
06	P42	+3VALWP/+5VALWP	10/14	Lin Will	Slove EMI.	Change PJP19 to PL23, and PR59, PR60 from 0 ohm to 2.2 ohm.	X02
07	P44	+1.5VSP/+0.75VSP	10/14	Lin Will	Slove EMI.	Change PJP29 to PL24.	X02
08	P45	+1.1VS_VTTP	11/20	Lin Will	Low down the power consumption.	Change PR232 from 43.2K to 9.31K, and add PR327 2.7K.	X03
09	P41/P42	Charger +3VALWP/+5VALWP	11/20	Lin Will	1206 size shortage issue.	Change SE142475K8L(1206) to SE000006R8L(0805), and SE041224K8L(1206) to SE000005Z8L(0603).	X03
10	P42	+3VALWP/+5VALWP	11/20	Lin Will	When adapter inserts and pulls out quickly two twice, it will make TPS51427 out of electricity.	Add PR91,PR92,PR93 100K, PQ24 TP0610K, PQ10 DTC115EUA, and PD17 1SS355TE-17. But all unpop. Change PU5 to ISL6237, and add PJP24.	X03

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